International Journal for Modern Trends in Science and Technology
Volume: 03, Issue No: 03, March 2017
ISSN: 2455-3778
http://www.ijmtst.com

# PV Based Load Resonant for Boost Converter by Using Quasi Z-Source Network

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**To Cite this Article**
Nendragamti Pavan Kumar, "PV Based Load Resonant for Boost Converter by Using Quasi Z-Source Network", *International Journal for Modern Trends in Science and Technology*, Vol. 03, Issue 03, 2017, pp. 01-06.

## ABSTRACT

*In this paper a novel resonant dc-dc converter utilizing fell semi Z-source system is displayed. Diverse routine converters like arrangement thunderous converter and LLC resonant converters experiences extensive variety of info voltage and load varieties since exchanging recurrence was shifted. The proposed Z-source dc-dc converter can limit changing recurrence range to lead high converter productivity. The fell QZS system is gotten from conventional semi Z source inverter by including one diode, one inductor, and two capacitor to it. The fell QZSI works as venture up converter in typical mode i.e. yield voltage is expanded three circumstances than information voltage and for lift mode yield voltage is expanded ten circumstances input voltage. At the point when contrasted and customary QZSI proposed arrangement lessens shoot through obligation cycle by more than 30% at same voltage support figure. Two phase QZSI in shoot through and non-shoot through modes are portrayed hypothetically. This proposed framework is dependability is incredibly made strides. Its exhibitions are confirmed utilizing MATLAB\SIMULINK instrument to check hypothetical presumptions and equipment is actualized by utilizing sunlight based vitality as info.*

**KEYWORDS:** *PV Cell, DC-DC converter, Series Resonant Converter, LLC resonant converter, Z-source network.*

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## I. Introduction

Semi Z source detached Dc/Dc converters have been generally utilized as a part of medium power applications for conveyance framework. Converters have no exchanging misfortune and are more reasonable for high recurrence operations. Because of wellbeing and element execution necessities, the interface converter ought to be acknowledged inside the dc/dc/air conditioning idea. This implies low voltage from the source first goes through the front-end venture up dc/dc converter with the galvanic segregation; in this manner, the yield dc voltage is reversed in the three-stage inverter and separated to conform to the forced principles and necessities (second dc/air conditioning stage).

Disseminated control era, when completely actualized, can give dependable, high caliber, and minimal effort electric power is engaged. To expand the power thickness of the converter, a three-stage helper air conditioning joins (a three-stage inverter and a three-stage confinement transformer) and a three-stage VDR are proposed. The plan of the front-end disengaged dc/dc converter is most testing since this stage is the principle giver of interface converter proficiency, weight, and general measurements. In the primary case [Fig. 1(a)], the assistant lift converter ventures up the differing FC voltage and battery voltage to a specific steady voltage level (40–80 Vdc) and supplies the info terminals of the secluded dc/dc converter. Its principle downsides are drawn from the multistage vitality transformation structure,

i.e., convoluted control and security calculations and diminished unwavering quality because of the expanded number of exchanging gadgets. An immediate stride up dc/dc converter without info voltage pre direction [Fig. 1(b)] is less difficult in control and security. Because of the diminished number of exchanging gadgets, the converter has a tendency to have better productivity and dependability. The changing voltage from the FC is gone through the high-recurrence inverter to the progression up seclusion transformer. This paper is dedicated to another power circuit topology to be executed in the front-end dc/dc converter to give compelling voltage venture up in the entire scope of information voltage and load varieties by utilizing separation transformer.

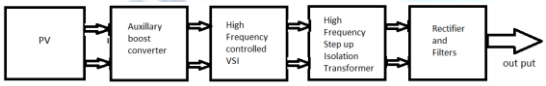



***Fig 1(a) with double converter***

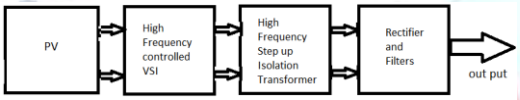
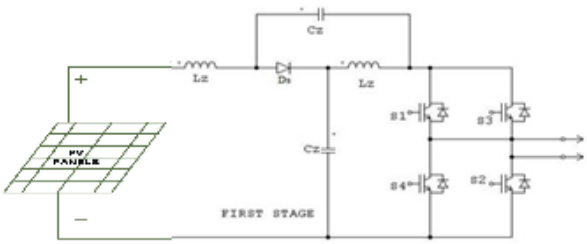



***Fig 1. (b) With single converter***

## II. Traditional Z-Source Converter

To overcome the above issues of the customary V-source and I-source converters, an impedance-source (or impedance-encouraged) control converter (contracted as Z-source converter) and its control strategy for actualizing dc-to-air conditioning, air conditioning to-dc, air conditioning to-air conditioning, and dc-to-dc control transformation is executed and clarified in. The extraordinary component of the Z-source inverter is that the yield air conditioning voltage can be any an incentive in the vicinity of zero and endlessness paying little heed to the info voltage. That is, the Z-source inverter is a buck–boost inverter that has an extensive variety of possible voltage. Fig 2 demonstrate the conventional voltage nourished ZSI and the proposed voltage bolstered QZSI, separately. Lz and Cz are associated in a shape to give an impedance source




coupling the converter to dc source/load (can be either voltage and current source/stack). Thus, DC source can be a battery, diode, rectifier, thyristor converter, power device, an inductor, a capacitor or a blend of those. With the conventional ZSI, the one of a kind LC and diode arrange associated with the inverter connect adjust the operation of the circuit, permitting the shoot-through state. This system will adequately shield the circuit from harm when the shoot-through happens and by utilizing the shoot-however express, the (semi ) Z-source arrange supports the dc-interface voltage acquired. The ZSI has intermittent info current in the lift mode; while the information current of the QZSI is persistent because of the info inductor L1, which will altogether lessen input stretch. Two high-effectiveness vitality reusing zero-voltage electronic burdens (ELs) in view of a Z-source converter can be additionally clarified in.Fig.2.Traditional Z source network

The proposed Z-source full converter has both the buck and lift work, in view of its working states i.e.output voltage (Vdc) of the converter can be found either inside or amidst the info voltage (Vin) go. The yield voltage of inverter (Vo) can be associated with rectifier through arrangement thunderous circuit is talked about in. The resonant tank is associated between Z source and load in fig 3 it shows three working modes in light of thunderous recurrence.

$fr=1/2\pi LrCr$

**Normal mode (Vin=Vdc, fsw = fr):**

In normal mode switching frequency is set equal to the frequency of tank circuit. The tank frequency can be defined as

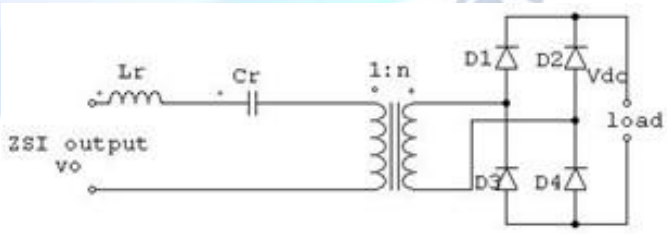



Fig.3.Resonant tank connection

Where Lr and Cr are tank circuit elements. There is no circulating current and all switches are turned on and off at perfect zcs and zvs condition.so maximum efficiency will be achieved here.

**Buck mode (Vin>Vdc ,fr<fsw<fsw.max ):**

In this mode, the proposed converter operates as conventional SRC and controls *Vo* by increasing

*fsw*from *fr*to its maximum switching frequency ( *fsw*.max ). However, unlike the SRC, frequency range of the proposed converter can be much narrower than that of the SRC because the input voltage range in buck mode is narrower than that of the SRC[4 .7]. As a consequence, light load efficiency of the proposed converter can be improved significantly.

**Boost mode (Vin<Vdc,fsw = fr):**

In this mode, *fsw*is fixed to *fr*, same as the normal mode operation mentioned above. In this mode, however, the proposed converter uses switch shoot through to boost *Vo*. This Z source network uses shoot through mode to boost the voltage and the circuit can be analyzed to determine the boost voltage factor.

## III. Step Up DC/DC Converter with Cascaded QZS-Network

The fell (two-arrange) QZS-system could be determined by the including of one diode, one inductor, and two capacitors to the customary semi Z-source inverter (QZSI). The proposed fell QZSI acquires every one of the upsides of the conventional arrangement (voltage lift and buck works in a solitary stage, persistent info current, and enhanced unwavering quality). Besides, when contrasted with the customary QZSI, the proposed arrangement decreases the shoot-through obligation cycle by more than 30% at a similar voltage support figure. In customary QZSI, the shoot through states are utilized to help the attractive vitality put away in the dc-side inductors Lz and Lz without short circuiting the dc capacitors Cz and Cz.

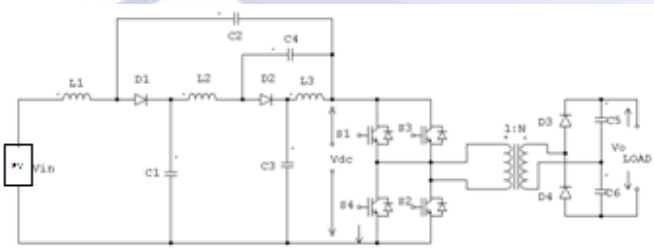



***Fig.4.Cascaded two stage QZS network and a voltage doubler without resonant element***

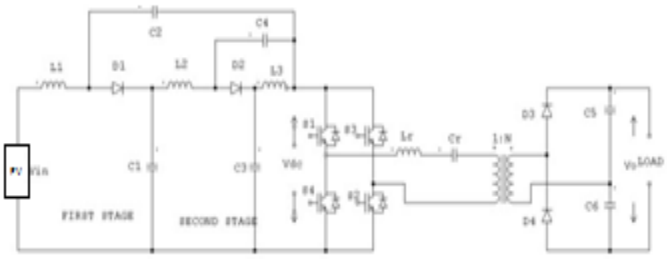



***Fig.5.Cascaded two stage QZS network and a voltage doubler with resonant element***

The topology proposed (Fig. 4 and 5) utilizes the voltage-fed QZSI with a cascaded QZS-network and continuous input current, a high-frequency step-up isolation transformer, and a voltage-doubler rectifier. It analyzes the design of the two-stage QZSI, whereas the design and operation of the transformer–rectifier stage of the converter remain the same as those with traditional isolated full-bridge converters. To regulate the varying input voltage, the front end/cascaded QZSI has two different operating modes:

**Modes of Operation**

- Non shoot-through(active mode)and
- Shoot through.(boostmode)

**None shoot through mode:**

Amid light load conditions QZSI performs just voltage buck work, when the yield voltage of FC or any fluctuating dc source achieves greatest esteem. The inverter is controlled in an indistinguishable way from with the conventional VSI, using just the dynamic states when one and just a single switch in each stage leg behaviors is given in. VSI applies one of the six non-zero changing vectors to the heap while drawing a heap dependant current from its info source.

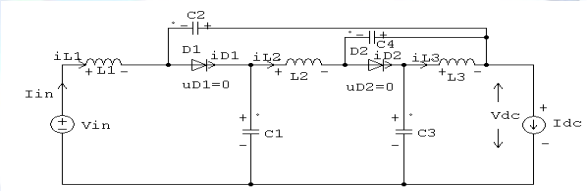
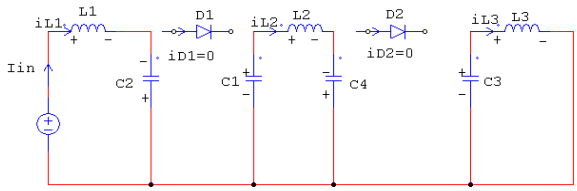



***Fig 6.Equivalent circuits during (a) non shoot through (active) state (b)shoot through state***

Considering the power adjust between the information and yield of the VSI, the current drawn from the impedance arrange amid this state can be spoken to by a steady current source Idc . The transistors in the full-connect setup are controlled then again in sets (s1 and s4 or s2 and s3,) with 180◦-stage moved control signs are inform in. In this working mode, the obligation cycle of inverter switches would never surpass 0.5.

**Shoot through mode:**

When the input voltage drops below some predefined value, the QZSI starts to operate in the shoot- through mode. In order to boost the input voltage during this mode, a special switching




state—the shoot-through state—is implemented in the pulse width modulation (PWM) inverter control. The cascaded QZS network makes the shoot-through states possible, effectively protecting the circuit from damage is indicated in. Moreover, the shoot through states are used to boost the magnetic energy stored in the dc-side inductors *L*1, *L*2, and *L*3 without short circuiting the dc capacitors *C*1,*C*4.

This increase in the magnetic energy, in turn, provides the boost of the voltage seen on the inverter output during the active states. The equivalent circuit of the two-stage QZSI during the shoot-through states is shown in Fig. 6(b).

**Operation without resonant elements Lr and Cr:**

In the circuit diagram for cascaded Z source network is shown. This circuit will be operated in active and shoot through states. Therefore output of voltage doubler (Vo) is increased at 3- 9 times than input voltage (Vin). In active state Vo is three times Vin and nine times greater than Vin during Boost mode due to the presence of two stage Z source network.

**Operation with resonant elements Lr and Cr:**

Figure 5 is referred for operation of two stage Z source network with tank circuit. This circuit also operated in active and shoot through states. Therefore output of voltage doubler (Vo) is increased at 4-10 times than input voltage (Vin).compared to without resonant elements this mode increases output voltage.

## IV. Simulation Results

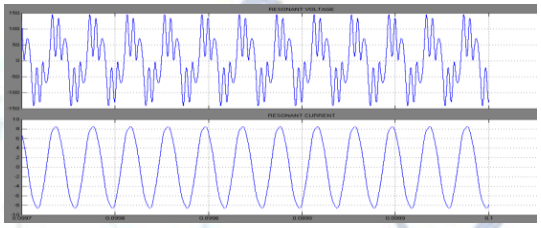
The supply voltage can be varied from 50V-150V as per load and value of source inductor and capacitors. The resonant frequency is set to 49.3 kHz.

**Traditional Z source network**:

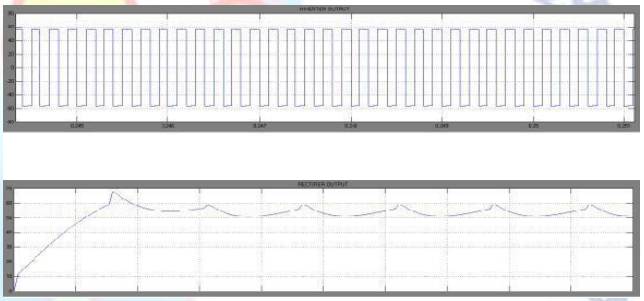
The Supply voltage of converter is 40V and achieved output voltage is 800V in boost mode. And in buck mode the 80V is applied voltage and achieved output voltage is 60V as per the load variations. In normal mode the 80V is supplied voltage and same is achieved. And the experimental parameters are source inductor=5e-6, capacitor=.54e-6, isolation transformer=1:1 turns ratio, series resonant of series inductor =74e-6H, capacitor= 141e- 09F. All the simulations in traditional are observed using resistor load (20Ω-100Ω) in fig 8. Larger source inductor and capacitor can be used at 100e-6H, 220e-6F respectively.

Normal mode inverter and rectifier output

a. **A. Buck mode inverter and rectifier output**



***B. Boost mode inverter output voltage and current***



***C.Boost mode rectifier output voltage and current.***

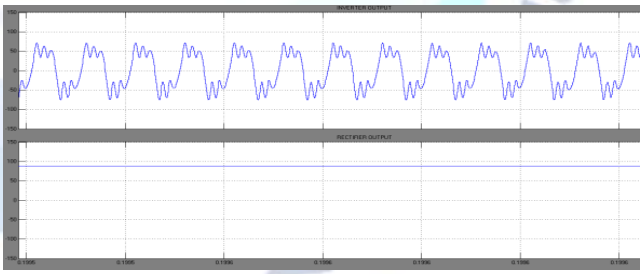
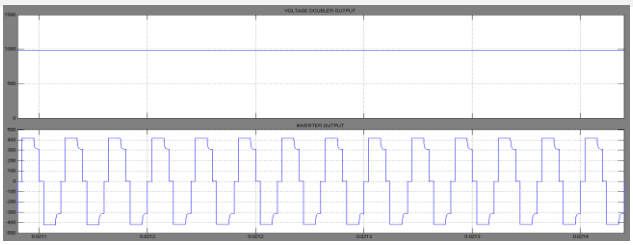


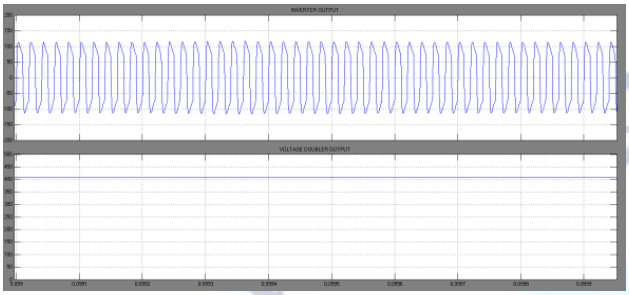
Fig 7.output waveforms of traditional Z network in normal (a) , buck (b) and boost(c&d) mode

**Cascaded two stage Z source network:** The values of source inductor and capacitor are same in traditional network. But input voltage is 100V for both active and shoot through states. This circuit does not work in boost mode

**Without tank circuit:** For 100V supplied voltage output of voltage doubler is varied from 300V to 900V in normal (a) and boost (b) modes in fig 8.

Normal mode voltage doubler and inverter output

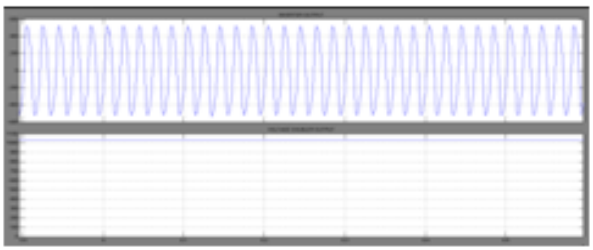




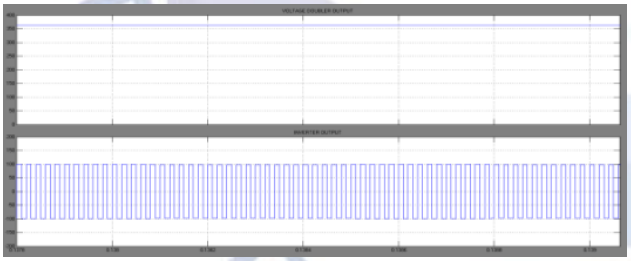
(b) Boost mode voltage doubler and inverter output

**Cascaded two stage ZSI network without tank circuit and With tank circuit:**

The value of series Lr and Cr are same in traditional Z source network. For 100V supplied voltage, output of voltage doubler is varied from 400V to 1000V in normal (a) and boost (b) modes in fig 9.



*a) Normal mode voltage doubler and inverter output*



b) Boost mode voltage doubler and inverter output

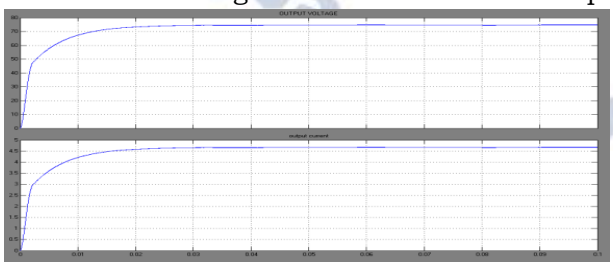


***Fig.9.Cascaded two stage ZSI network with tank circuit***

## V. CONCLUSION

This paper has introduced conventional and fell Z source coordinate with/without resonant components. To form the fell QZS-arrange, one diode, one inductor, and two capacitors were added to the customary voltage-encouraged QZSI. The yield voltage is supported 3 to 10 times more prominent than information voltage in two phase fell QZSI organize. The novel setup acquires every one of the benefits of conventional arrangements (voltages lift and buck works in a solitary stage, consistent information current, and enhanced unwavering quality). The voltage-sustained QZSI with the fell QZS-organize lessened the shoot-through obligation cycle by more than 30% at a similar voltage help variable and part worries as the conventional QZSI. The proposed fell QZSI can be connected to all dc/air conditioning, air conditioning/dc, air conditioning/air conditioning, and dc/dc control transformation plans. To further decline the shoot-through obligation cycle at a similar voltage help figure, the quantity of phases of the QZS-system could be expanded. Because of expansion of full circuit two phase fell system likewise has justifies in customary system with tank circuit. The proposed two phase converter can be short-and open circuited without harming exchanging gadgets. Consequently, it is extremely solid to EMI and its heartiness and unwavering quality is essentially made strides. The yield voltage is be more prominent than information voltage at a wide range so relevant is private applications. Hence it is an extremely alluring circuit topology when the info voltage and load scope of the converter is wide. The proposed fell QZSI converter can accomplish great productivity over the whole information and load extend.

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