



Modified MPPT Converter with PI Control Technique for DC-to-DC Converter Application

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ABSTRACT

An dc- dc converter is used in MPPT (Maximum Power Point Trackers) to improve match between solar panel and loads. Solar (PV) modules are controlled by an electronic control system that permits each module to produce the maximum amount of electrical energy possible. Using MPPT technology, the modules can operate at varying electrical operating points, allowing them to deliver maximum power. Additional power collected from the modules is used to expanded battery charging current. MPPT takes a gander at the results of the boards and analyses the battery voltage to determines absolute maximum power that panel can produce by using an algorithm. By converting it to the best voltage, it gets the most AMPS into the battery. Most MPPTs are 92-97% capable of converting data. When compared to a normal PWM controller, we gain 20 to 45 percent power in the winter and 10-20 percent in the summer. Solar panels provide the majority of their electricity all at once known as Maximum Power Point (MPP).MPP is impacted by the progressions in ecological factors like sun irradiation and temperature. In order to produce maximum power, solar cells must always reach their MPP despite changes in climate. For this reason, the applied controllers on solar panels employ the MPPT algorithm. Our objective is to design an MPPT method based on the PI logic controller (PI) that will reach MPP in a short period of time and will adapt to change environmental conditions when necessary. It focuses primarily on writing and re-enactments. The MPPT based on PI seems to be able to detect and reach MPP regardless of whether the irradiance changes slowly or quickly.

KEYWORDS:SolarPhotovoltaic (PV), Flyback Converter, LUO converter and PI Controller

1. INTRODUCTION

The DC/DC converter widely used to manage switch mode DC power supply. The contribution of these converters is an unregulated DC voltage, which is obtained by PV exhibits and subsequently, it will vary because of changes in radiation and temperature. In those converters, the regular DC yield voltage need to managed to be likened to the correct really well worth

albeit the information voltage is evolving. According to power perspective, yield voltage tenetwithinside the DC/DC converter carried outvia way of means of usually converting how an awful lot power is retained from the supply and that infused into the heap, that is consequently restrained via way of means of the overall lengths of the retention and infusion stretches.

These two fundamental cycles of energy assimilation and infusion establish an exchanging cycle. Naturally talking, assuming that the energy storage capacity of the converter is too small or the switching period is relatively too long, then the converter would have transmitted the stored energy to the load before the following cycle starts. This presents a idling period quickly following the infusion span, during which the converter isn't performing any specific task. The converter can along these lines work in two distinct modes relying on its energy stockpiling limit and the general length of the exchanging time frame. These two modes are known as the irregular conduction and continuous modes.

The DC/DC help converter just necessitates four parts: inductor, the electronic switch, diode and capacitor. This converter works in 2 unique modes depending upon its energy stockpiling restrict and the general duration of the changing time frame these two working modes are known as irregular conduction mode DCM and CCM there are benefits of PVs like lowering environmental pollution offering easy strength and generating energy everywhere there's sunlight. Voltage and Power of PV panels depend on several factors like irradiation and temperature.

To examine the photovoltaic module's reaction under the exceptional conditions the system turned into operated under variable irradiations and temperatures. PV structures include PV modules and DC-DC converters. Flyback converter is one of the converters used to control the voltage of PV panel.

The topology of flyback converters is totally based on buck-boost converters but there can be a difference in some of the insulated transformers rather than storage inductors. The transformer presents insulation and due to the variable turns ratio, the output voltage can be adjusted easily.

The development of industry new control methods developed, PI control is one of the control methods, which is easy to implement the system, also it has effective results. In this study, PI manage carried out a switch of the converter to adjust the duty cycle PWM to fee the battery with a steady voltage method, the system changed into built and analysed in MATLAB / Simulink.

2. EXISTING CONVERTER

MPPT are so critical in PV systems to build their efficiency. Various approach has been proposed to achieve maximum power. PV modules are good for establishing under different environmental conditions.

The structure contains a PV solar module associated with a DC-DC Buck-Boost converter.

The framework has been capable of aggravation in the PV temperature and irradiance level. The simulation result shows the proposed MPT could follow the maximum power unequivocally and viably in all conditions attempted.

Correlation of different performance limits, e.g., following productiveness and response season of the system indicating that proposed approach provides better proficiency and better final performance when compared with perturbation and observation methods.

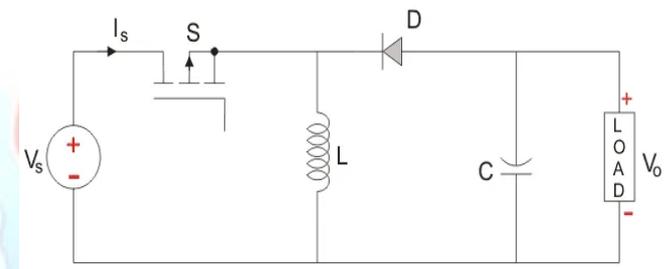


FIG1. EQUIVALENT CIRCUIT OF BUCK-BOOST CONVERTER

It clarifies functioning method for modern-day framework. A buck converter is sort of DC-to-DC converter in any other case referred to chopper that has a result voltage quantity this is both greater outstanding then or now no longer precisely the statistics voltage size.

It is applied to "step up" the DC voltage, like a transformer for AC circuits. It corresponds to a flyback converter using a single inductance instead of a transformer. Two distinct geographies are known as buck-boost converters. DC-DC converters is defined as choppers.

Here we can study the Buck-Boost converter which can act as DC-DC Step-Down converter or a DC-DC Step-Up converter by relying on the duty cycle, D.

Diode is tied, back to step of switch from the power supply, to a capacitor and the heap, and the 2 are related similarly as displayed withinside above FIG.1. The switch is controlled wide to be here and thereby

using Pulse Width Modulation (PWM). PWM can be time- based or recurrence-based totally.

3. PROPOSED CONVERTER

The Flyback Converter obtained PV board is constrained by PI technologies. A PV system can be considered as a direct (direct current) source with the goal that one of the DC-to-DC converters, flyback converters, is utilized to manage voltage and current of the PV Panel. The PI Controller applied a switch (MOSFET) to direct the duty cycle (PWM) with consistent voltage technique.

a) DESIGN AND ANALYSIS OF FLYBACK CONVERTER

To regulate the voltage of the PV panel, a flyback converter is used in the proposed system. The flyback converter operation is shown in FIG.2 and FIG. 3. When the switch (MOSFET) is turned on, the primary side of the transformer is connected to the PV panel, and energy is stored in the primary winding.

Since the polarities of primary and secondary windings are different, the series diode of the secondary side gets reverse biased and is not conducting Shown in FIG. 2,3. When the switch becomes off, stored energy withinside the magnetic core and air gap transfer to the secondary winding, the magnetic inductance current flows via the secondary side shown in FIG.3.

There are three modes to operate a Flyback converter; Continuous connection mode (CCM), Discontinuous conduction mode (DCM), and critical operation mode. Because of the fast response to the load current and variations of input voltage, in this system, CCM is preferred.

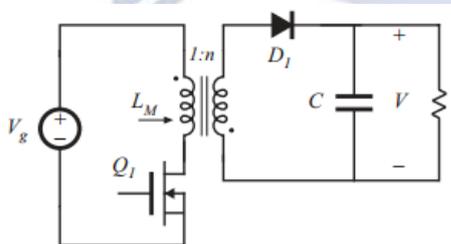


FIG 2. FLYBACK CONVERTER SWITCH ON MODE

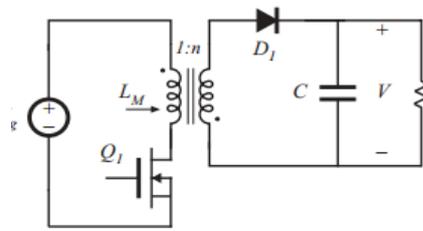


FIG 3. FLYBACK CONVERTER SWITCH OFF MODE

The equation for energy stored at the air gap,

$$E = \frac{1}{2} \times L_p \times I_p^2.$$

Where,

L_p is the primary winding inductor

I_p primary winding current.

It is known that mean voltage on the inductors of transformer is zero at steady-state condition,

$$(V_p - V_{sch}) \times t_{on} = (V_o + V_d) \times t_{off} \times n,$$

Where,

V_{pv} is input voltage (PV),

V_{sch} is switch voltage,

V_o is output voltage,

V_d is diode anode-cathode voltage,

N is turn ratio of transformer,

t_{on} is switch on time,

t_{off} is switch off time.

The equations of a flyback converter in DCM (discontinuous conduction mode) are following

For CCM the period of the switch is

$$T = t_{on} + t_{off}$$

(a) OPERATION SCENARIO OF FLYBACK FLYBACK CONVERTER OPERATION AND PRINCIPLE WHEN THE SWITCH IS TURNED ON

At this point, primary winding charges and currents can be generated while the switch is on.

$$V_{in} - V_L - V_s = 0$$

FLYBACK CONVERTER OPERATION AND PRINCIPLE WHEN THE SWITCH IS TURNED OFF

The operation of the flyback converter in the OFF state when OFF is focused on the secondary. A secondary current may flow when the switch is off.

When doing KLV,

$$V_L \text{ secondary} - V_D - V_{out} = 0$$

b) LUO CONVERTER

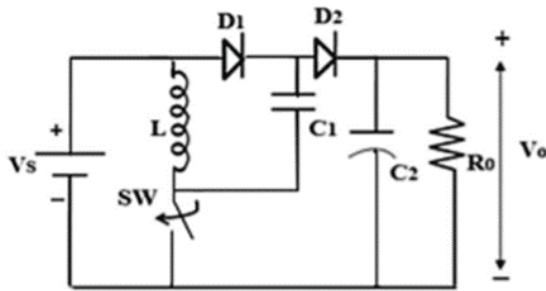


FIG 4. CIRCUIT DIAGRAM OF LUO CONVERTER

Perhaps the most notable strategy used primarily to construct electronic circuits is voltage swing technology, which has recently found a wider range of applications in DC power converters. Such type of converter is the Super lift-Luo converter, which is outlined in Figure 4. Using voltage swing technology, this type of converter tuning results in a positive voltage at a particular voltage and achieves first quadrant operation with a higher voltage gain compared to boost converters. In Figure 4, V_s is the positive DC input voltage, V_o is the corresponding output voltage, SW is the n-channel MOSFET, D1 and D2 are the freewheel diodes, L is the inductor, and C1 and C2 are the capacitors. The converter works with CCM and achieves high power density. Moreover, all the parameters are assumed to be the ideal ones. State model of the positive result Luo converter the dynamic equations describing the system functions are state affine and time invariant which is depicted as follows,

$$\dot{x}(t) = A_1(t) + B_1 V_s(t), \text{ sw}=1$$

$$\dot{x}(t) = A_2(t) + B_2 V_s(t), \text{ sw}=0$$

Here $SW=1$ implies the on condition of the switch and $SW=0$ means the off condition of the switch. A_1 , A_2 , B_1 , and B_2 are the coefficient matrices given by,

$$A_1 = \begin{bmatrix} 0 & 0 \\ 0 & \frac{-\alpha}{R_o C_2} \end{bmatrix}; A_2 = \begin{bmatrix} 0 & \frac{\alpha-1}{L} \\ 1-\alpha & \frac{\alpha-1}{R_o C_2} \end{bmatrix}; B_1 = \begin{bmatrix} \alpha \\ L \end{bmatrix} \text{ and } B_2 = \begin{bmatrix} 2-2\alpha \\ L \end{bmatrix}$$

By the discrete equal is obtained by using the corresponding related equations,

$$G' = e^{AT_s} H' = \int_{\tau=0}^{T_s} e^{A\tau} d\tau B$$

$$C'_d = CD'_d = D$$

Where G_0 , H_0 , C_0 d, and D_0 d are the coefficient matrices for the discrete-time system. For discrete signs,

the situations describing super lift Luo converter are

$$G' = \begin{bmatrix} 0.9998 & -0.0334 \\ 0.0111 & 0.9990 \end{bmatrix}$$

$$H' = \begin{bmatrix} 0.0133 \\ 0.0001 \end{bmatrix}$$

gotten as

4. BLOCK DIAGRAM OF PROPOSED SYSTEM

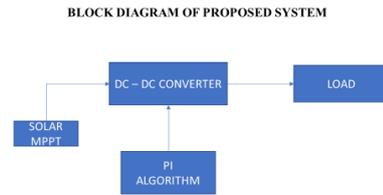


FIG 5. BLOCK DIAGRAM OF PROPOSED SYSTEM

MPPT is attached to the DC-to-DC converter. It includes converters which can be flyback converter and Luo converter. From the PI algorithm pulse is generated and given to the converter it generates a pulse to load.

The proposed flyback combined Luo converter design calculations by considering the switch position are mentioned below.

At ideal condition,
 $V_s = 0$ (voltage drop)
 Then $V_{in} - V_L = 0$
 Where, $V_L = V_{in}$
 $V_L = L_p di/dt$
 $di = (V_{in} / L_p) \times dt$

By applying integration on each side, we get,

Current on the primary winding,
 $I_{pri} = (V_{in} / L_p) T_{on}$
 Where V_{in} = input voltage

L_p = inductance of the primary winding
 T_{on} = while the switch is turned ON

The total energy stored in the primary winding,
 $E_{pri} = \frac{1}{2} I_{pri}^2 \times L_p$

When the switch is OFF, we get
 $V_L(\text{sec}) - V_D - V_{out} = 0$

The diode voltage drop might be 0 at ideal condition

$V_L(\text{sec}) - V_{out} = 0$

$$V_L(\text{sec}) = V_{out}$$

$$V_L = L_s di/dt$$

$$di = (V_L \text{ sec} / L_s) / dt$$

$$\text{Since } V_L \text{ sec} = V_{out}$$

Hence,

$$di = (V_{out} / L_s) \times dt$$

By making use of integration, we get

$$I_{sec} = (V_{sec}/L_s) (T - T_{on})$$

The total energy transferred is expressed as

$$E_{sec} = \frac{1}{2}[(V_{sec}/L_s) \cdot (T - T_{on})]^2 \cdot L_s$$

Where V_{sec} = secondary winding voltage (total output voltage on the load)

L_s = inductance of the secondary winding

T = PWM signal period

T_{on} = switch ON time

5. MODELING AND SIMULATION

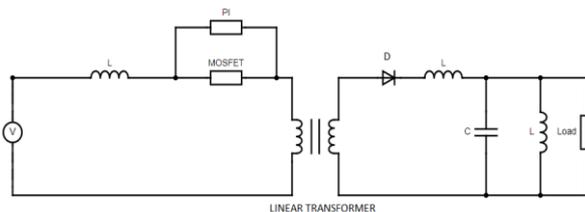


FIG 6. CIRCUIT DIAGRAM OF PROPOSED CONVERTER

The proposed flyback joined Luo converter is extraordinarily basic and contains electrical components like a proposed flyback combined Luo transformer, switch, rectifier, channel, and a managed gadget to drive the switch and reap regulation. The switch is used to turn ON and OFF the primary circuit. The PWM signal from the regulator controls the activity of the switch. In the extra part of the proposed flyback combined Luo transformer plans, FET or MOSFET, semiconductor is applied because of the switch.

The rectifier corrects the voltage of the secondary winding to get DC output and heap from the secondary winding of the transformer. The capacitor channels the rectifier yield voltage and expands the DC yield stage according to the ideal application.

The proposed flyback joined Luo transformer is applied as an inductor to save magnetic energy. It is deliberate as a coupled inductor, which is going approximately because of the essential and

non-compulsory winding. It works at excessive frequencies 50KHz.

It is important to consider the proposed flyback consolidated Luo converter plan estimations of the turns proportion, obligation cycle, and the flows of primary and secondary windings. Since the turns share may also have an effect on the current coursing through the primary and secondary winding and moreover the duty cycle. At the point whilst the turn proportion is excessive, then, at that factor, the duty cycle moreover seems to be excessive, and the modern-day is going through the vital and non-compulsory winding abatements.

As the transformer applied within the circuit is a custom kind, it is impractical to get a super transformer with a turn share nowadays. Henceforth through choosing the transformer with the appropriate value determinations and closer to the vital reviews may also make up for the difference within the voltage and output.

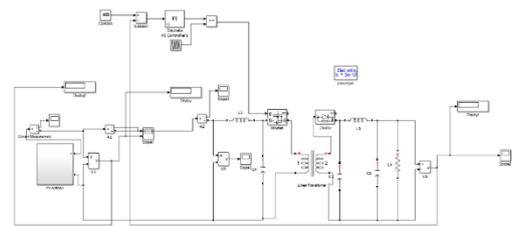


FIG 7. SIMULATION MODEL

The above given FIG 7. is a simulation diagram of flyback converter combined Luo converter design is very simple. It contains electrical components like a flyback combined Luo transformer, switch, rectifier, filter, and a control device to drive the switch and achieve regulation.

The switch is used to ON and OFF the primary circuit, which can magnetize or demagnetize the transformer. The PWM signal from the controller controls the operation of the switch. In flyback combined Luo transformer designs, FET or MOSFET or a basic transistor is used as the switch. Rectifier rectifies the voltage of the secondary winding to get pulsating DC output and disconnects the load from the secondary winding of the transformer. The capacitor filters the rectifier output voltage and increases the DC output level as per the desired application. The flyback combined Luo transformer is used as an inductor to

store the magnetic energy. It is designed as a two coupled inductor, which acts as the primary and secondary winding.

6. SIMULATION RESULT

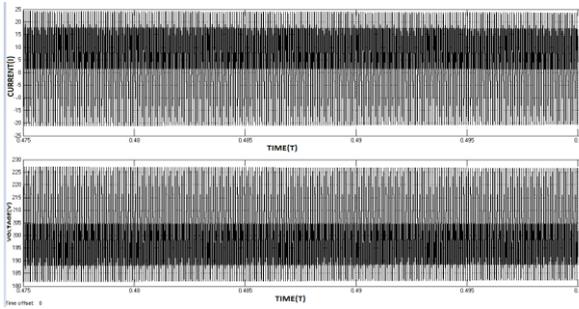


FIG 8. INPUT CURRENT AND INPUT VOLTAGE WAVVFORM

The above given FIG 8 is a waveform of input current and input voltage. Input voltage=201.8V and Input current=4.15 A.

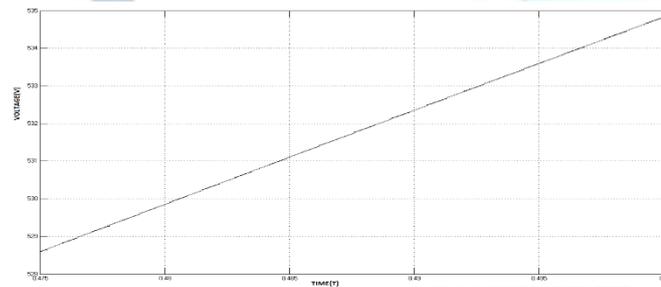


FIG 9 OUTPUT VOLTAGE WAVEFORM

The given FIG 9 is a waveform of output voltage. When input voltage 201.3 V is given output voltage of the system is 534.8 V and current remains constant. The X axis of the waveform is time(t) and the Y axis of the waveform is voltage(v).

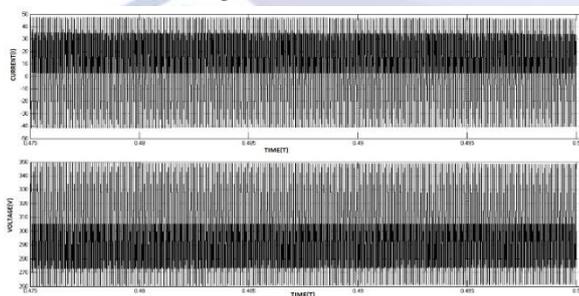


FIG 10. INPUT CURRENT AND INPUT VOLTAGE WAVVFORM

The above given FIG 10 is a waveform of input current and input voltage. Input voltage=299.9 V.

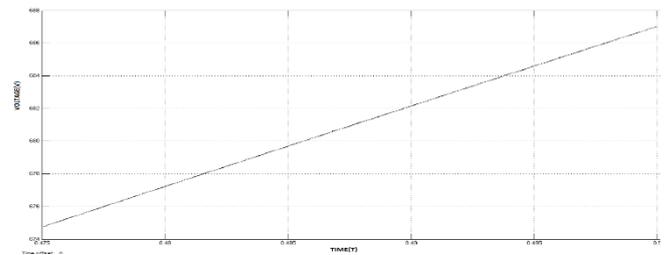


FIG 11 OUTPUT VOLTAGE WAVEFORM

The given FIG 11 is a waveform of output voltage. At the point when input voltage 299.9V V is given output voltage of the framework is 687 V and current remains steady. The X axis of the waveform is time(t) and the Y axis of the waveform is voltage(v).

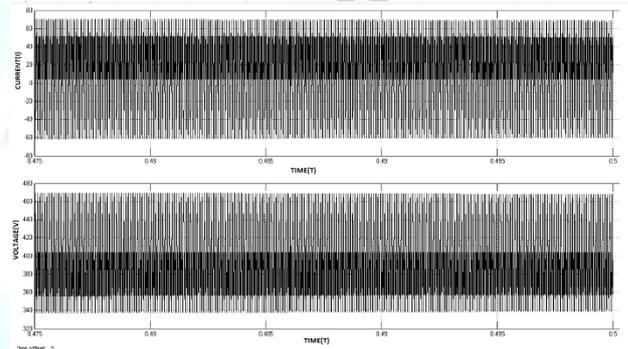


FIG 12. INPUT CURRENT AND INPUT VOLTAGE WAVVFORM

The above given FIG 12 is a waveform of input current and input voltage.

Input voltage=396 V.

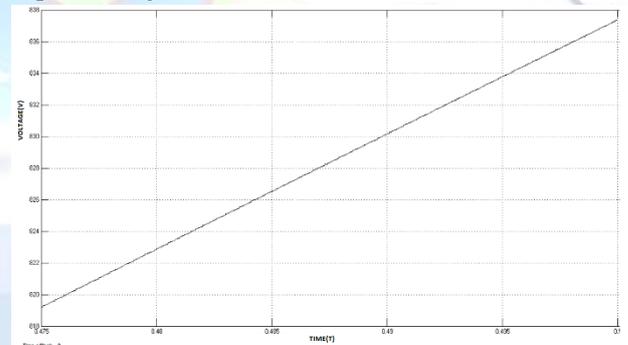


FIG 13 OUTPUT VOLTAGE WAVEFORM

The given FIG 13 is a waveform of output voltage. At the point when input voltage 396 V is given output voltage of the framework is 837.3 V and current remains steady. The X axis of the waveform is time(t) and the Y axis of the waveform is voltage(v).

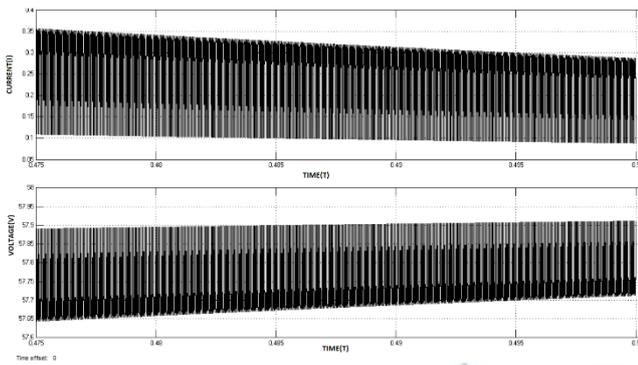


FIG 14. INPUT CURRENT AND INPUT VOLTAGE WAVVFORM

The above given FIG 14is a waveform of input current and input voltage.

Input voltage=57.75 V.

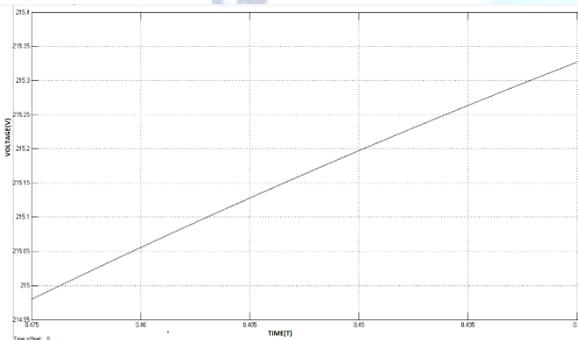


FIG 15 OUTPUT VOLTAGE WAVEFORM

The given FIG 15 is a waveform of output voltage. At the point when input voltage 57.75 V is given output voltage of the framework is 215.3 V and current remains steady. The X axis of the waveform is time(t) and the Y axis of the waveform is voltage(v).

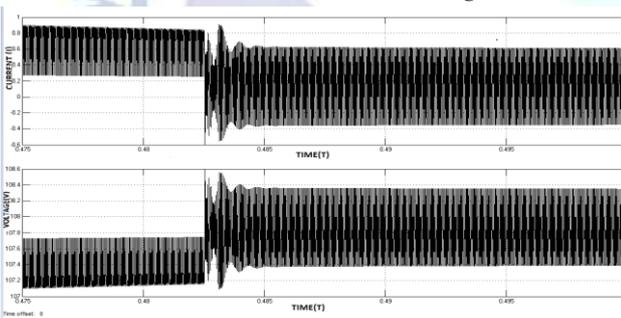


FIG 16. INPUT CURRENT AND INPUT VOLTAGE WAVVFORM

The above given FIG 16 is a waveform of input current and input voltage.

Input voltage=108.2 V.

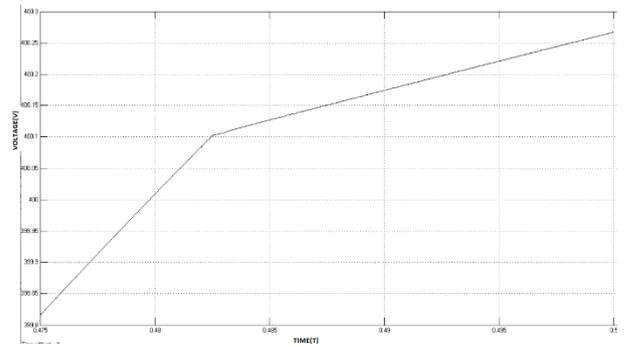


FIG 17 OUTPUT VOLTAGE WAVEFORM

The given FIG 17 is a waveform of output voltage. At the point when input voltage 108.2 V is given output voltage of the framework is 400.3 V and current remains steady. The X axis of the waveform is time(t) and the Y axis of the waveform is voltage(v).

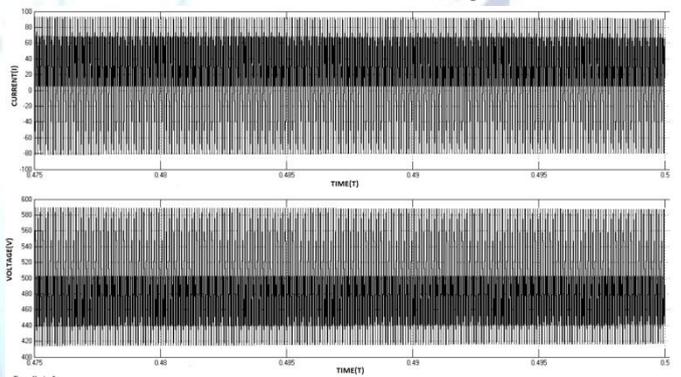


FIG 18. INPUT CURRENT AND INPUT VOLTAGE WAVVFORM

The above given FIG 18 is a waveform of input current and input voltage.

Input voltage=492.1 V.

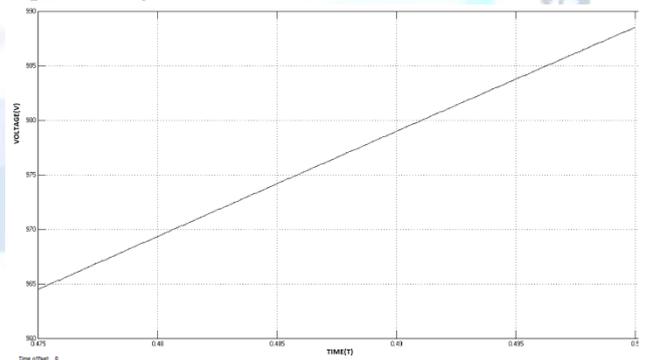


FIG 19 OUTPUT VOLTAGE WAVEFORM

The given FIG 19 is a waveform of output voltage. At the point when input voltage 492.1 V is given output voltage of the framework is 988.5 V and current remains steady. The X axis of the waveform is time(t) and the Y axis of the waveform is voltage(v).

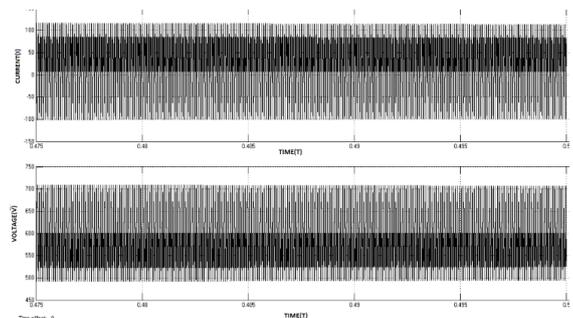


FIG 20. INPUT CURRENT AND INPUT VOLTAGE WAVVFORM

The above given FIG 20 is a waveform of input current and input voltage.

Input voltage=588.2 V.

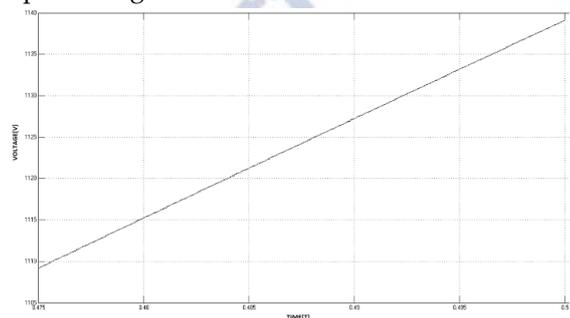


FIG 21 OUTPUT VOLTAGE WAVEFORM

The given FIG 21 is a waveform of output voltage. At the point when input voltage 588.2 V is given output voltage of the framework is 1139 V and current remains steady. The X axis of the waveform is time(t) and the Y axis of the waveform is voltage(v).

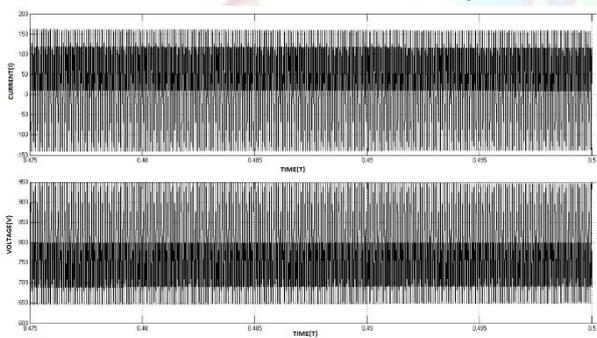


FIG 22. INPUT CURRENT AND INPUT VOLTAGE WAVVFORM

The above given FIG 22is a waveform of input current and input voltage.

Input voltage=780.5 V.

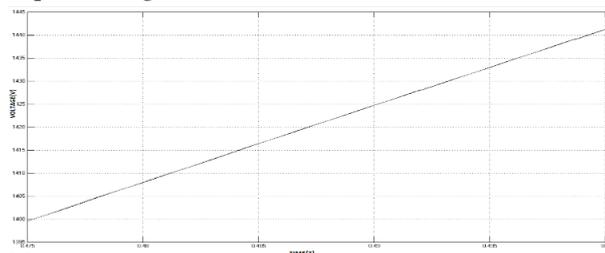


FIG 23. OUTPUT VOLTAGE WAVEFORM

The given FIG 23 is a waveform of output voltage. At the point when input voltage 780.5 V is given output voltage of the framework is 1441 V and current remains steady. The X axis of the waveform is time(t) and the Y axis of the waveform is voltage(v).

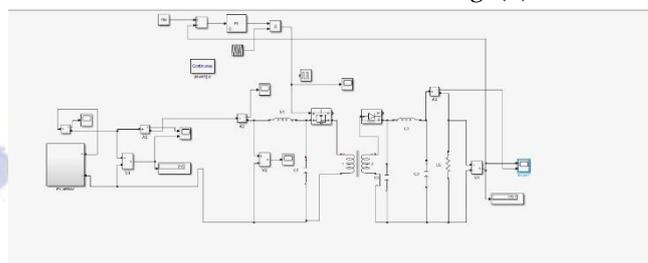


FIG 24. SIMULATION DIAGRAM WITH CONTINUOUS

The above given FIG 24.is a simulation diagram of flyback converter combined Luo converter with continuous design is very simple. It contains electrical components like a flyback combined Luo transformer, switch, rectifier, filter, and a control device to drive the switch and achieve regulation.



FIG 25. OUTPUT VOLTAGE WAVEFORM OF CONTINUOUS

The given FIG 25 is a waveform of output voltage. Input voltage=216V and Input current=1.7A.

When input voltage 216V is given then output voltage of the system is 172V and current remains constant. The X axis of the waveform is time(t) and the Y axis of the waveform is voltage(v).

TABLE 1 SIMULATION OUTPUTS

S.NO	INPUT VOLTAGE	OUTPUT VOLTAGE
1	201.8	534.8
2	299.9	687
3	396	837.3
4	57.75	215.3
5	108.2	400.3
6	492.1	988.5

7	588.2	1139
8	780.5	1441

7. HARDWARE PROCESS



FIG 26. HARDWARE WORKING

The proposed flyback combined luo converter design is very simple and contains electrical components like a proposed flyback combined luo transformer, switch, rectifier, filter, and a control device to drive the switch and achieve regulation.

The switch is used to ON and OFF the primary circuit, which can magnetize or demagnetize the transformer. The PWM signal from the controller controls the operation of the switch. In most of the proposed flyback combined luo transformer designs, FET or MOSFET or a basic transistor is used as the switch.

Rectifier rectifies the voltage of the secondary winding to get pulsating DC output and disconnects the load from the secondary winding of the transformer. The capacitor filters the rectifier output voltage and increases the DC output level as per the desired application.

The proposed flyback combined luo transformer is used as an inductor to store the magnetic energy. It is designed as a two coupled inductor, which acts as the primary and secondary winding. It operates at high frequencies of nearly 50KHz.

In this hardware circuit, we have given input from a solar panel, and the output is connected to the rectifier load. This is a combined structure of a luo converter and a flyback converter is connected to a microcontroller and output is shown in a multimeter. MOSFET switch is used in this circuit. It is the main component of power electronics circuits. MOSFET driver is dedicated to integrated circuits that are used to drive gate terminals of power switches both in the low side and high side configuration.

One switch used in the operating system When a switch is turned OFF, then the inductor is energized at that time input is equal to output. When a switch is in turned OFF state pulse is coherent at that time input is 15.5A and output is 15.9 A. TLP250 IC is used to convert 5v signal to 12v signal.

When a switch is turned on, the inductor is de-energized. when the system is de-energized at that time voltage boost multi times depends upon inductor range and switching frequency. Input is 10 A and Output is 75 A it boosts 7.5 times. when we increase the switching frequency and inductor turns, we can boost more voltage.

Then dspic30f2010 microcontrollers are used in a system. It generates only one PWM because one switch is used in this circuit. Generated PWM is given to the isolated driver through MOSFET.

INPUT VOLTAGE

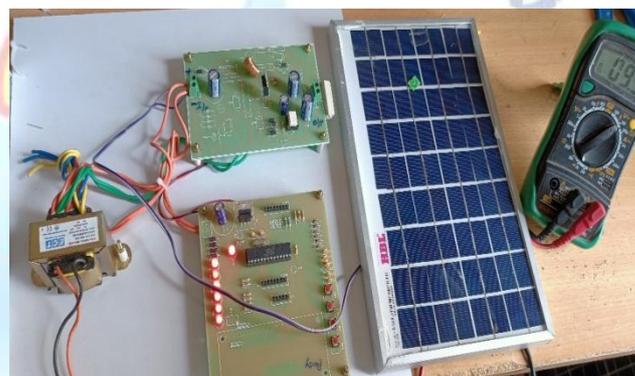


FIG 27. INPUT VOLTAGE OF PROPOSED SYSTEM

When a switch is turned ON, the inductor is de-energized at that time voltage boost multi times depends upon inductor range and switching frequency. Multimeter is used to measure voltage. Input voltage of the system is 9.9A.

OUTPUT VOLTAGE



FIG 28. OUTPUT VOLTAGE OF PROPOSED SYSTEM

When a switch is turned ON, the inductor is de-energized at that time voltage boost multi times depends upon inductor range and switching frequency. Multimeter is used to measure voltage. Output voltage of the system is 75.5A.

PULSE WAVEFORM

The above given FIG 29 is a pulse waveform. The pulse wave is a kind of non-sinusoidal waveform that includes square wave and similarly periodic but asymmetrical waves. It is a term used in synthesizer programming and is typical waveform available on many synthesizers. Above given waveform is a switching pulse.

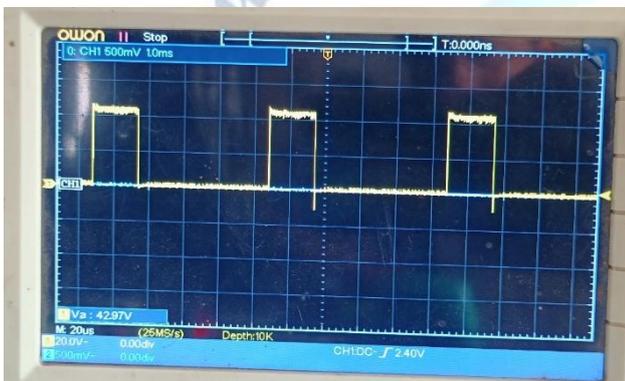


FIG 29. PULSE WAVEFORM

8. CONCLUSION

The practical block diagram and the certain MPPT algorithms the use of PI is provided on this project. The solar cell efficiently modelled the use of MATLAB 2016. The overall performance of the proposed MPPT strategies is examined via means of simulation at different irradiation and temperature. The effects are analysed and compared. Solving the problem of energy availability in DC buses may be achieved via means of the use of a version of the battery unit garage system. The power supply of the PV array and AC grid, one after the other charging every battery unit. By the use of PID controllers, it may correctly boost the voltage at the unidirectional converter enhance and bidirectional converter enhance. The simulation effects show that PI for FLYBACK-primarily based totally MPPT controller is simple, fast, efficient, and has quicker convergence. The MPPT's mentioned are around 92-97% efficient. Typically, 20 to 35% energy advantage in iciness and 10-20% in summertime season may be achieved. The virtual examination, simulated effects, and

experimental readings display that the discrete observer controller designed for Flyback primarily based totally LUO converter accomplishes inflexible voltage regulation, excellent vibrant characteristics, and higher effectiveness. The Flyback-based Luo converter with a discrete observer controller may be used for any of the packages like present-day transportable digital devices, laptop peripherals, clinical equipment, energy component correction, or gasoline cell packages.

Conflict of interest statement

Authors declare that they do not have any conflict of interest.

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