

Hysteresis Current Controlled Power Quality Enhancement of Grid Connected RES using BESS Based DSTATCOM

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Abstract: The advancement of power electronic-based sensitive loads drives the power utilities' devotion to power quality issues. The voltage disturbance could be happening due to fault conditions, switching of loads, energizing of transformers, or integration of highly intermittent energy sources such as PV systems. This research work attempts to enhance the voltage fluctuation of a sensitive load connected to a grid-integrated PV system using a battery-based DSTATCOM. The proposed battery energy storage-based STATCOM has two separate controlling stages that are implemented at the DC-DC buck/boost converter of the battery and voltage source converter (VSC) system. Charging and discharging of the battery is operated based on the state-of-charge (SOC) value of the battery and the measured root mean square (RMS) voltage at the point of common coupling (PCC). The VSI of the STATCOM detection and reference generation control is done appropriately. Simulation results establishes that technique proposed in this manuscript is found to be effective for PQ improvement in hybrid power system during operational events of outage and synchronization of renewable energy (RE) generator.



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INTRODUCTION

The increasing number of renewable energy sources and distributed generators requires new strategies for the operation and management of the electricity grid in order to maintain or even to improve the power-supply reliability and quality. In addition, liberalization of the grids leads to new management structures, in which trading of energy and power is becoming increasingly important. The power-electronic technology plays an important role in distributed generation and in integration of renewable energy sources into the electrical grid, and it is widely used and rapidly expanding as these applications become more integrated with the grid-based systems.

During the last few years, power electronics has undergone a fast evolution, which is mainly due to two factors. The first one is the development of fast semiconductor switches that are capable of switching quickly and handling high powers. The second factor is the introduction of real-time computer controllers that can implement advanced and complex control algorithms. These factors together have led to the development of cost-effective and grid-friendly converters.

Renewable energy source (RES) integrated at distribution level is termed as distributed generation (DG). The utility is concerned due to the high penetration level of intermittent RES in distribution systems as it may pose a threat to network in terms of stability, voltage regulation and power-quality (PQ) issues. Therefore, the DG systems are required to comply with strict technical and regulatory frameworks to ensure safe, reliable and efficient operation of overall network. With the advancement in power electronics and digital control technology, the DG systems can now be actively controlled to enhance the system operation with improved PQ at PCC. However, the extensive use of power electronics based equipment and non-linear loads at PCC generate harmonic currents, which may deteriorate the quality of power [1], [2].

Generally, current controlled voltage source inverters are used to interface the intermittent RES in distributed system. Recently, a few control strategies for grid connected inverters incorporating PQ solution have been proposed. In [3] an inverter operates as active inductor at a certain frequency to absorb the harmonic current. But the exact calculation of network inductance

in real-time is difficult and may deteriorate the control performance. A similar approach in which a shunt active filter acts as active conductance to damp out the harmonics in distribution network is proposed in [4]. In [5], a control strategy for renewable interfacing inverter based on – theory is proposed. In this strategy both load and inverter current sensing is required to compensate the load current harmonics.

The non-linear load current harmonics may result in voltage harmonics and can create a serious PQ problem in the power system network. Active power filters (APF) are extensively used to compensate the load current harmonics and load unbalance at distribution level. This results in an additional hardware cost. However, in this paper authors have incorporated the features of APF in the, conventional inverter interfacing renewable with the grid, without any additional hardware cost. Here, the main idea is the maximum utilization of inverter rating which is most of the time underutilized due to intermittent nature of RES. It is shown in this paper that the grid-interfacing inverter can effectively be utilized to perform following important functions:

- 1) Transfer of active power harvested from the renewable resources (wind, solar, etc.).
- 2) Load reactive power demand support.
- 3) Current harmonics compensation at PCC and
- 4) Current unbalance and neutral current compensation in case of 3-phase 4-wire system.

Moreover, with adequate control of grid-interfacing inverter, all the four objectives can be accomplished either individually or simultaneously. The PQ constraints at the PCC can therefore be strictly maintained within the utility standards without additional hardware cost.

The paper is arranged as follows: Section II describes the system under consideration and the controller for grid-interfacing inverter. A digital simulation study is presented in Section III. Extensive experimental results are discussed in Section IV and, finally, Section V concludes the paper.

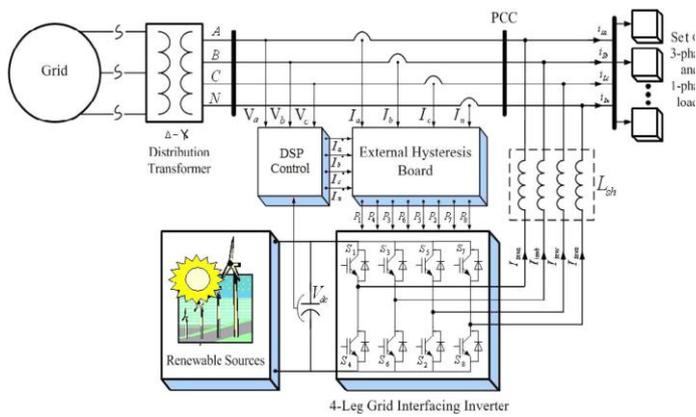


Fig.1 Schematic of proposed renewable based distributed generation system

DESCRIPTION OF THE SYSTEM

A. Topology

An Active power filters are power electronic devices that cancel out unwanted harmonic currents by injecting a compensation current which cancels harmonics in the line current. Shunt active power filters compensate load current harmonics by injecting equal-but opposite harmonic compensating current. Generally, four-wire APFs have been conceived using four leg converters [5]. This topology has proved better controllability [6] than the classical three-leg four-wire. In this paper, it is shown that using an adequate control strategy, even with a three phase four-wire system, The topology of the investigated APF and its interconnection with the grid is presented in Fig. 1. It consists of a three-leg four-wire voltage source inverter. In this type of applications, the VSI operates as a current controlled voltage source. The proposed system is Three Phase Four wire which consists of wind turbine system connected to the dc-link of a grid interfacing inverter as shown in Fig. 1. The voltage source inverter is a key element of a wind system as it interfaces the renewable energy source to the grid and delivers the generated power. The Wind Turbine system is connected to grid with an inverter coupled to dc-link. The dc-capacitor decouples the Wind Turbine system from grid and also allows independent control of converters on either side of dc-link.

B. Wind Turbine

Wind turbines are used to convert the wind power into electric power. Electric generator inside the turbine converts the mechanical power into the electric power.

Wind turbine systems are available ranging from 50W to 2-3 MW. The energy production by wind turbines depends on the wind velocity acting on the turbine. Wind power is used to feed both energy production and consumption demand, and transmission lines in the rural areas.

Wind turbines can be classified with respect to the physical features (dimensions, axes, number of blade), generated power and so on. For example, wind turbines with respect to axis structure: horizontal rotor plane located turbines, turbines with vertical or horizontal spinning directions with respect to the wind. Turbines with blade numbers: 3-blade, 2-blade and 1- blade turbines.

On the other hand, power production capacity based classification has four subclasses.

- Small Power Systems
- Moderate Power Systems
- Big Power Systems
- Megawatt Turbines

C. Voltage Source Converter (VSC)

A Voltage Source Converter (VSC) is a power electronic device that connected in shunt or parallel to the system. It can generate a sinusoidal voltage with any required magnitude, frequency and phase angle. It also converts the DC voltage across storage devices into a set of three phase AC output voltages. It is also capable to generate or absorbs reactive power. If the output voltage of the VSC is greater than AC bus terminal voltages, is said to be in capacitive mode. So, it will compensate the reactive power through AC system. The type of power switch used is an IGBT in anti-parallel with a diode. The three phase four leg VSI is modelled in Simulink by using IGBT.

D. Controller for Active Power Filter

The dc link voltage, V_{dc} is sensed at a regular interval and is compared with its reference counterpart V_{dc}^* . The error signal is processed in a PI-controller. The output of the pi controller is denoted as I_m . The reference current templates (I_a^* , I_b^* , and I_c^*) are obtained by multiplying this peak value (I_m) by the three-unit sine vectors (U_a , U_b and U_c) in phase with the three source voltages. These unit sine vectors are obtained from the three sensed line to neutral voltages. The reference grid neutral current (I_n^*) is set to zero, being

the instantaneous sum of balanced grid currents. Multiplication of magnitude I_m with phases ($U_a, U_b,$ and U_c) results in the three phase reference supply currents ($I_a^*, I_b^*,$ and I_c^*).

The grid synchronizing angle (θ) obtained from phase locked loop (PLL) is used to generate unity vector template as

$$U_a = \text{Sin}(\theta) \tag{1}$$

$$U_b = \text{Sin}(\theta - \frac{2\pi}{3}) \tag{2}$$

$$U_c = \text{Sin}(\theta + \frac{2\pi}{3}). \tag{3}$$

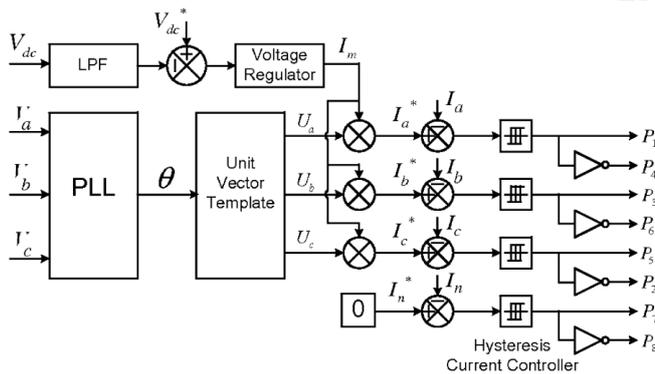


Fig.2. Control Scheme

The instantaneous values of reference three phase grid currents are compute as

$$I_a^* = I_m \cdot U_a \tag{4}$$

$$I_b^* = I_m \cdot U_b \tag{5}$$

$$I_c^* = I_m \cdot U_c. \tag{6}$$

The neutral current is considered as

$$I_n^* = 0 \tag{7}$$

The reference grid currents (I_a^*, I_b^*, I_c^* and I_n^*) are compared with actual grid currents (I_a, I_b, I_c and I_n) to compute the current errors as

$$I_{aerr} = I_a^* - I_a \tag{8}$$

$$I_{berr} = I_b^* - I_b \tag{9}$$

$$I_{cerr} = I_c^* - I_c \tag{10}$$

$$I_{nerr} = I_n^* - I_n. \tag{11}$$

These error signals are given to hysteresis current controller then generates the switching pulses for six IGBTs of the grid interfacing inverter.

E. D-STATCOM

D-STATCOM is also capable to generate or absorbs reactive power. If the output voltage of the VSC is greater than AC bus terminal voltages, D-STATCOM is said to be in capacitive mode. So, it will compensate the

reactive power through AC system and regulates missing voltages. These voltages are in phase and coupled with the AC system through the reactance of coupling transformers.

Suitable adjustment of the phase and magnitude of the DSTATCOM output voltages allows effectives control of active and reactive power exchanges between D-STATCOM and AC system. In addition, the converter is normally based on some kind of energy storage, which will supply the converter with a DC voltage.

HYSTERESIS CURRENT CONTROL

The hysteresis current control (HCC) is the easiest control method to implement; it was developed by Brod and Novotny in 1985 . The shunt APF is implemented with three phase current controlled VSI and is connected to the ac mains for compensating the current harmonics. The VSI gate control signals are brought out from hysteresis band current controller. A hysteresis current controller is implemented with a closed loop control system and waveforms are shown in Fig .3. An error signal is used to control the switches in a voltage source inverter. This error is the difference between the desired current and the current being injected by the inverter . If the error exceeds the upper limit of the hysteresis band, the upper switch of the inverter arm is turned off and the lower switch is turned on. As a result, the current starts decaying.

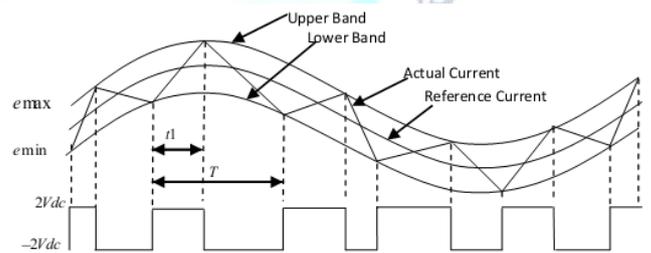


Fig. 3.Waveform of Hysteresis current controller

If the error crosses the lower limit of the hysteresis band, the lower switch of the inverter arm is turned off and the upper switch is turned on. As a result, the current gets back into the hysteresis band. The minimum and maximum values of the error signal are e_{min} and e_{max} respectively. The range of the error signal $e_{max} - e_{min}$ directly controls the amount of ripple in the output current from the VSI.

SIMULINK CIRCUIT

The performance of the proposed structure is assessed by a computer simulation that uses MATLAB Software. In order to verify the proposed control approach to achieve multi-objectives for grid interfaced DG systems connected to a 3-phase 4-wire network, an extensive simulation study is carried out using MATLAB/Simulink. A 4-leg current controlled voltage source inverter is actively controlled to achieve balanced sinusoidal grid currents at unity power factor (UPF) despite of highly unbalanced nonlinear load at PCC under varying renewable generating conditions. A RES with variable output power is connected on the dc-link of grid-interfacing inverter. An unbalanced 3-phase 4-wire nonlinear load, whose unbalance, harmonics, and reactive power need to be compensated, is connected on PCC. The waveforms of grid voltage (V_a, V_b, V_c), grid currents (I_a, I_b, I_c and I_n), unbalanced load current and inverter currents I_{la}, I_{lb}, I_{lc} and I_{ln} are shown in Fig. 6. The corresponding active-reactive powers of grid (P_{load}, q_d), load and inverter (P_{inv}, Q_{inv}) Positive values of grid active-reactive powers and inverter active-reactive powers imply that these powers flow from grid side towards PCC and from inverter towards PCC, respectively. The active and reactive powers absorbed by the load are denoted by positive signs. Initially, the grid-interfacing inverter is not connected to the network (i.e., the load power demand is totally supplied by the grid alone).

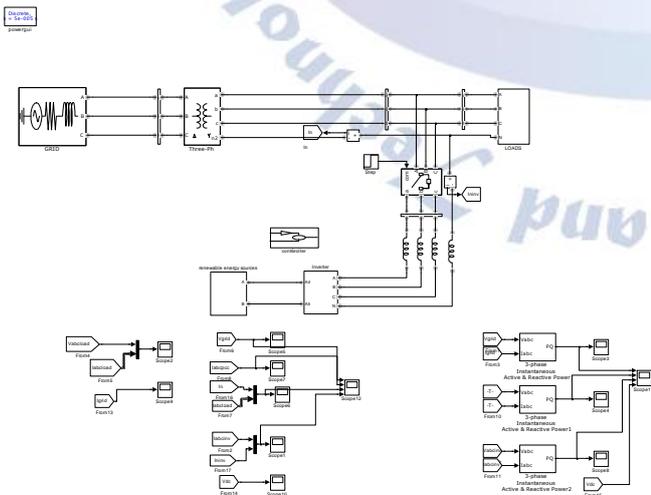


Fig 4: Simulation Diagram of proposed circuit

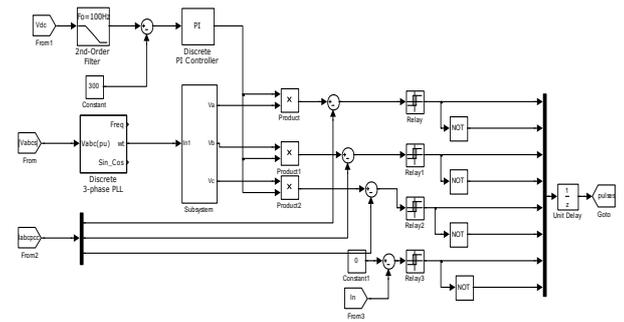


Fig 5: Simulation Diagram of Control circuit

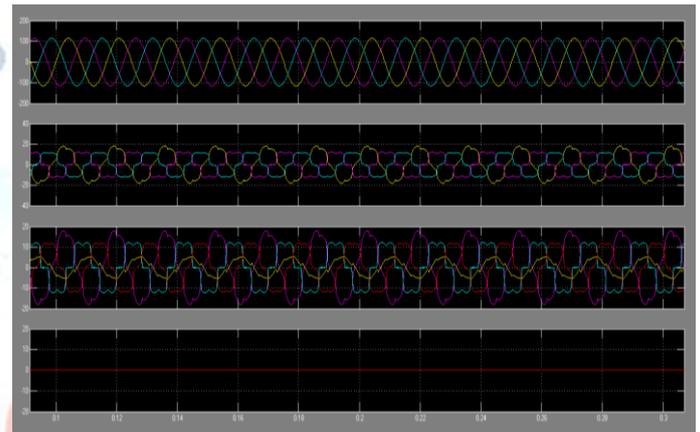


Fig 6: Simulation results: (a) Grid voltages, (b) Grid Currents (c) Unbalanced load currents, (d) Inverter Currents from 0.1 to 0.3 (before inverter switched ON)

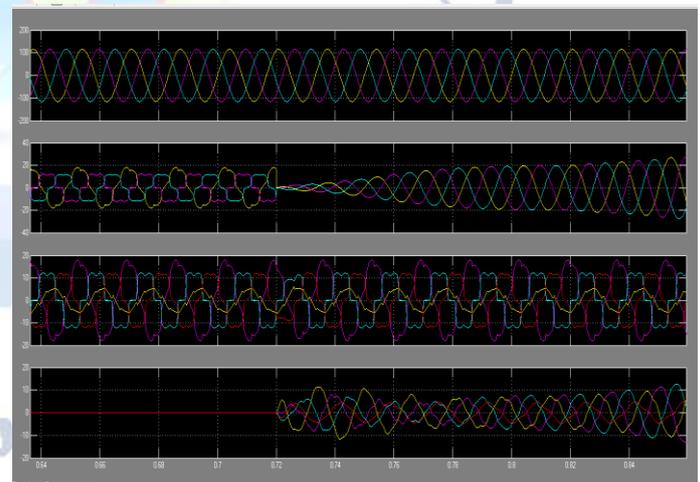


Fig 7: Simulation results: (a) Grid voltages, (b) Grid Currents (c) Unbalanced load currents, (d) Inverter Currents from 0.64 to 0.84 sec (inverter switched ON at 0.72 sec)

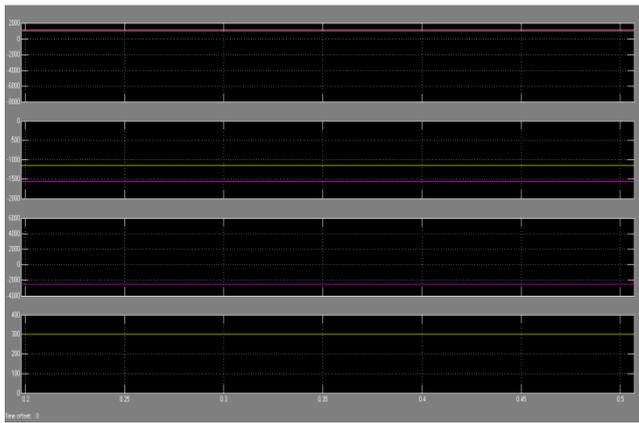


Fig 8:Simulation results: (a) PQ-Grid, (b) PQ-Load, (c) PQ-Inverter, (d) dc-link voltage from 0.2 to 0.5 sec

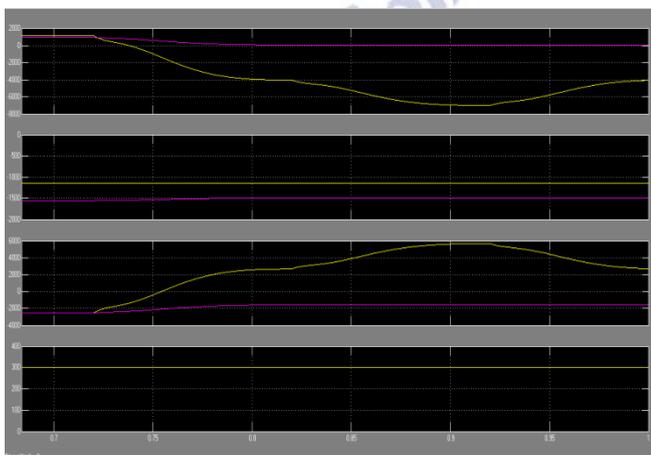


Fig 9:Simulation results: (a) PQ-Grid, (b) PQ-Load, (c) PQ-Inverter, (d) dc-link voltage from 0.7 to 1 sec

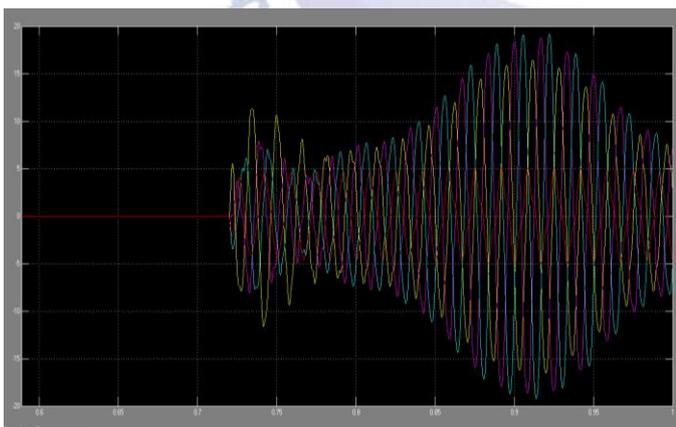
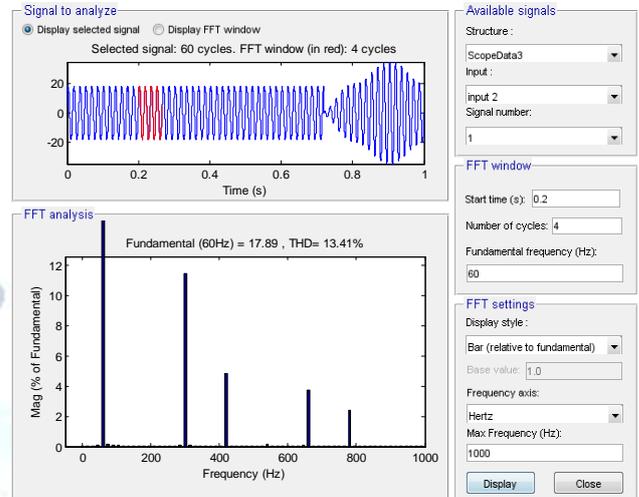


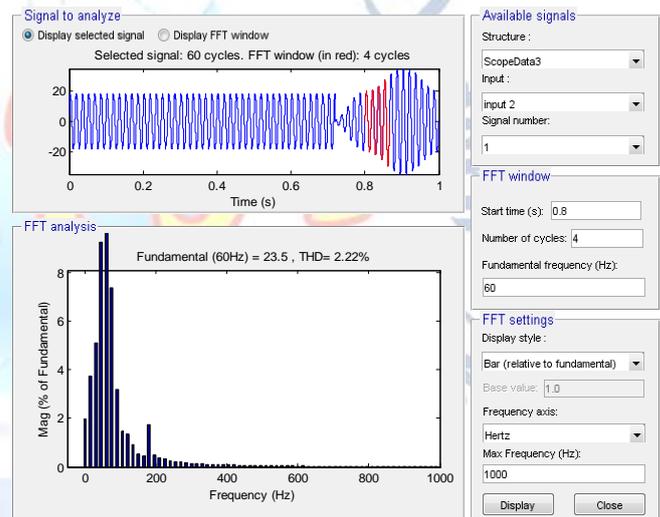
Fig 10: Inverter Current

Fig. 7 shows the source current, load current, inverter compensating current respectively. The inverter is turned on at 0.72 seconds. Fig. 7 (a) : it clearly indicates the source current from 0 to 0.7 sec represents the non-sinusoidal nature due to the presence of nonlinear load .At 0.7 seconds the nature of waveform is

sinusoidal this represents the inverter compensated the non sinusoidal wave to balanced sinusoidal wave. The load current waveform is shown in Fig. 7 (c) .The inverter supplies the compensating current that is shown in Fig. 7 (d).



THD without STATCOM (13.41%)



THD with STATCOM (2.22%)

CONCLUSIONS

This paper has presented a novel control of an existing grid interfacing inverter to improve the quality of power at PCC for a 3-phase 4-wire DG system, filters and D-STATCOM. It has been shown that the grid-interfacing inverter can be effectively utilized for power conditioning without affecting its normal operation of real power transfer. The filters can mitigate the harmonics in the output of the inverter and the D-STATCOM can compensate the voltage at the distribution level. The grid-interfacing inverter with the proposed approach can be utilized to:

- i) Inject real power generated from RES to the grid, and/or,
- ii) Operate as a shunt Active Power Filter (APF).

This approach thus eliminates the need for additional power conditioning equipment to improve the quality of power at PCC. Extensive MATLAB/Simulink simulation as well as the DSP based experimental results have validated the proposed approach and have shown that the grid-interfacing inverter can be utilized as a multi-function device.

The authors can conclude on the topic discussed and proposed. Future enhancement can also be briefed here.

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