International Journal for Modern Trends in Science and Technology
Volume: 03, Issue No: 06, June 2017
ISSN: 2455-3778
http://www.ijmtst.com

An Area Efficient Low Power High Speed Birotational Cordic Architecture

V.Suvarna[1] | Seema B Thakur[2] | A.Satyanarayana[3]

[1]PG Scholar, Department of ECE, Sanketika Institute of Technology and Management, Visakhapatnam, Andhra Pradesh, India.
[2,3]Assistant Professor, Department of ECE, Sanketika Institute of Technology and Management, Visakhapatnam, Andhra Pradesh, India.

To Cite this Article
V.Suvarna, Seema B Thakur and A.Satyanarayana, "An Area Efficient Low Power High Speed Birotational Cordic Architecture", *International Journal for Modern Trends in Science and Technology*, Vol. 03, Issue 06, June 2017, pp. 38-41.

ABSTRACT

CORDIC is an acronym for Coordinate Rotation Digital Computer. The CORDIC algorithm is a repetitive calculation approach ability of emerging different basic functions with a proper shift-and-add method Used to evaluate a large amount of functions. It has been used for many years for efficient implementation of vector rotation operations in hardware. It is executed merely by table look-up, shift, and addition operations. Rotation of vectors through fixed and known angles has many applications in animations, robotics, games, computer graphics and digital signal processing. In this paper we present improved method of shifting by using an alternate scheme by increasing the no. of barrel shifters with increasing pre shifting method and Fault Tolerance in Bi Rotational CORDIC circuits higher rate of accuracy in fixed and known rotations. The improvement in the fixed angle Rotation reducing the area- and Complexity in the application. From the basic architecture of cordic an fixed angle rotation is implemented by vector rotation. the rotation of vectors uncontrolled by the circuit till all rotations are completed it will results large system gain and unpredictable angles for effective operation of known angles in this paper angle correction ,Quadrant correction and gain correction is implemented.

Keywords: *CORDIC, Quadrant amendment, vectoring and rotation modes.*

I. INTRODUCTION

CORDIC is an acronym for Coordinate Rotation Digital Computer. It is classes of shift add algorithms for rotating vectors in a plane, which is usually used for the calculation of trigonometric functions. The CORDIC algorithm has become a widely used approach to elementary function evaluation when the silicon area is a primary constraint. The CORDIC algorithm was developed by J. E. Volder in 1959 for the computation of trigonometric functions. This has been recognized as the best compromise between the table look up approach requiring large memory, and polynomial approximation method, which is slow to converge to the desired precision.

In 1971, Walther has generalized this algorithm to implement rotation in circular, linear and hyperbolic coordinate systems. Since then it has been used as an elegant method to realize elementary functions such as multiplication, division, logarithmic and exponential functions in addition to the computation of two dimensional vector rotations. These transcendental functions are the core for many applications such as digital signal processing, graphics, image processing and kinematic processing. The implementation of CORDIC algorithm requires less complex hardware than the conventional method.

Angle recoding schemes, mixed-grain rotation and higher radix CORDIC have been developed for reduced latency realization. Parallel and pipelined CORDIC have been suggested for high-throughput computation.

II. Methodology

A CORDIC can be operated in two different modes, the vectoring and the rotation mode. In vectoring mode, coordinates (x,y) are rotated until y converges to zero. In rotation mode, initial vector (x,y) starts aligned with the x-axis and is rotated by an angle of θi every cycle, so sifter n iterations, θn is the obtained angle. All the trigonometric functions can be computed or derived from functions using vector rotations. The CORDIC algorithm provides an iterative method of performing vector rotations by arbitrary angles using only shift and add operations. The algorithm is derived using the general rotation transform.The CORDIC algorithm performs a planar rotation. Graphically, planar rotation means transforming a vector (x,y) into a new vector (x',y'). Vector V, came into image after anticlockwise rotation by an angle ϕ. From Fig.1 & 2, it can be observed that

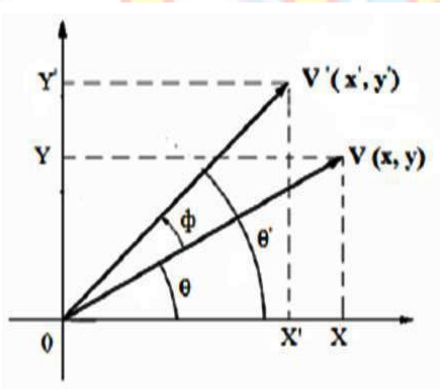


Fig.1 Rotation of vector V by an angle ϕ

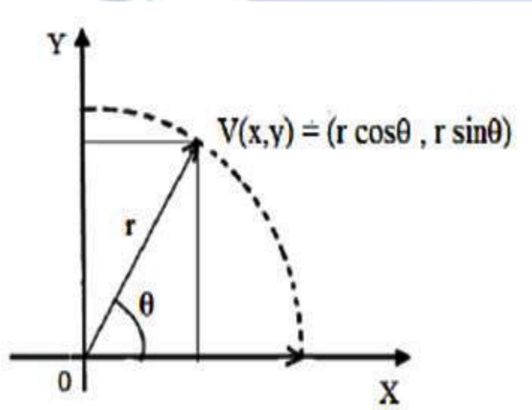


Fig.2 Vector v with magnitude r and phase θ

$$x' = x \cos\varphi - y \sin\varphi \quad (1a)$$
$$y' = y \cos\varphi + x \sin\varphi \quad (1b)$$

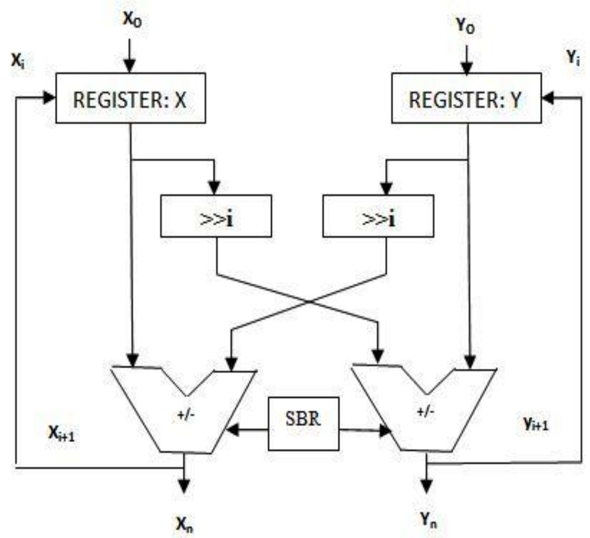
Which rotates a vector in a Cartesian plane by the angle φ. These can be arranged so that:

$x' = \cos\varphi.[\, x - y \tan\varphi \,]$ (1c) $y' = \cos\varphi.[\, y + x \tan\varphi \,]$ (1d)

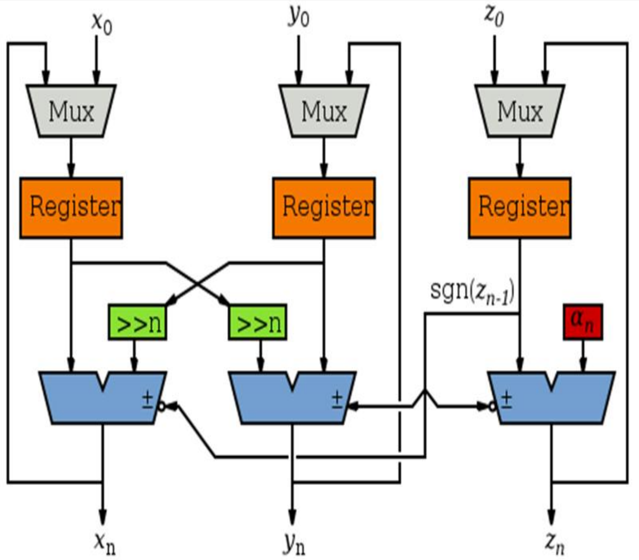
The computation of xi + 1 or yi + 1 requires an i bit right shift and add/subtract. If the function tan-1 (2-i) is pre computed and stored in table for different values of i, a single add/ subtract suffices to compute zi + 1. The tan-1 (2-i) values corresponding to 10 iterations are listed in Table 1. Each CORDIC iteration thus involves two shifts, a table lookup and three additions. If the rotation is done by the same set of angles (with + or – signs), then the expansion factor K, is a constant, and can be pre computed. For example to rotate by 30 degrees, the following sequence of angles be followed that add up to approximately 30 degree. 30.0 ≈ 45.0 –26.6 + 14.0 – 7.1 + 3.6 + 1.8 – 0.9 + 0.4 – 0.2 + 0.1 = 30.1

III. Proposed Method

The proposed CORDIC circuit is developed with optimization schemes for reducing the number of micro-rotations and for reducing the complexity of shifters for fixed angle vector rotation. A reference CORDIC circuit for fixed rotations according to equations (4.2) and (4.3) is shown in Fig. 4.2. x_0 and y_0 are fed as set/reset input to the pair of input registers and the successive feedback values x_i and y_i at the ith iteration are fed in parallel to the input registers. Note that conventionally we feed the pair of input registers with the initial values x_0 and y_0 as well as the feedback values x_i and y_i through a pair of multiplexers.

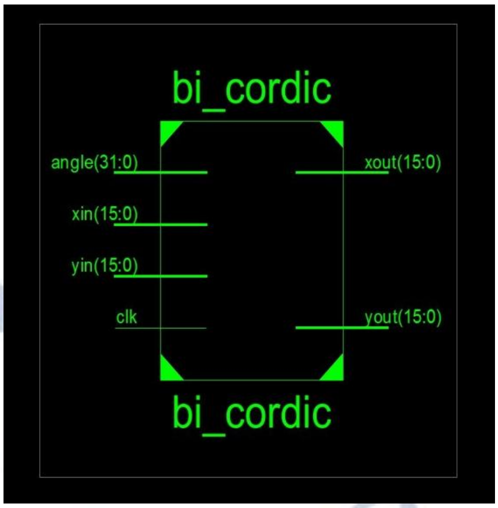


CORDIC circuit for fixed rotation



CORDIC Hardware

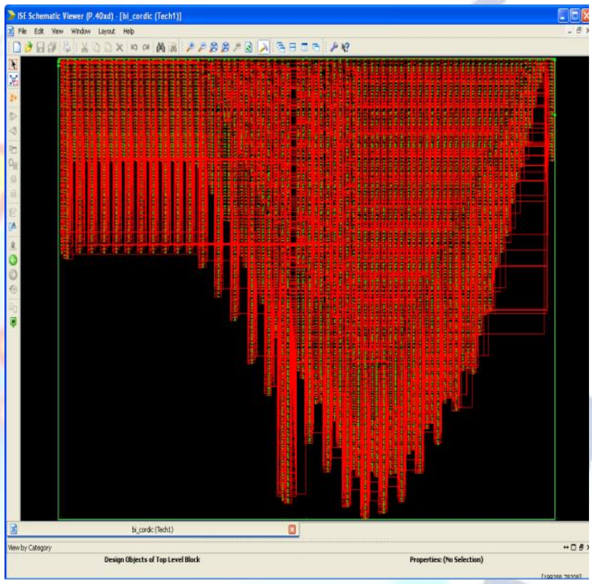
IV. Simulation Results



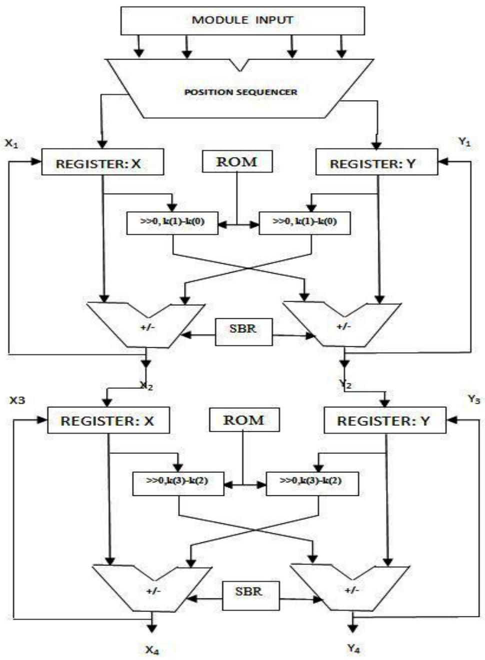
Block Diagram

A. Superior Bi-rotational CORDIC:

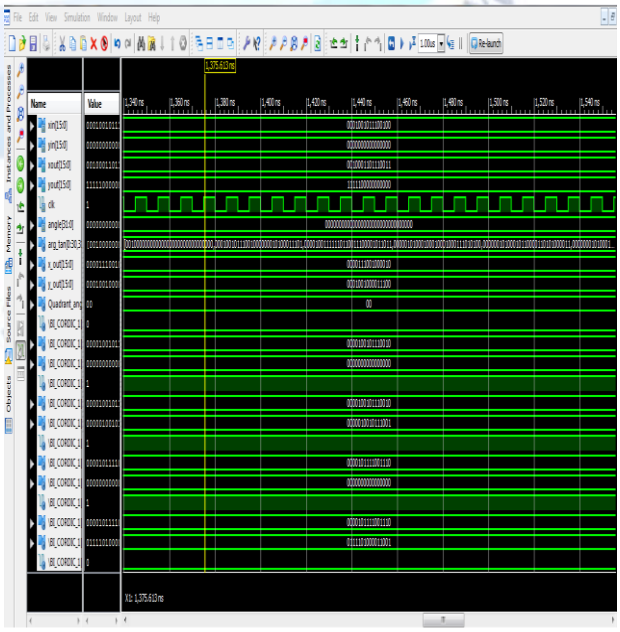
For reduction of adder complexity over the cascaded single-rotation CORDIC, the micro-rotations could be implemented by a cascaded bi-rotation CORDIC circuit. A two-stage cascaded superior bi-rotation CORDIC is shown in Fig. The first two of the micro-rotations out of the four-optimized micro rotations could be implemented by stage-1, while the rest two are performed by stage-2. The structure and function of the bi-rotation CORDIC is shown in Fig.4.8. For implementing six selected micro-rotations, we can use a three-stage-cascade of bi-rotation CORDIC cells. The three-stage superior bi-rotation cells could however be extended further when higher accuracy is required.



Technology Schematic (Full View)



Two-stage superior Bi-rotational CORDIC cell



Simulation Results of CORDIC Algorithm

V. Conclusion

The Superior Bi-rotational CORDIC is attractive for the calculation of fixed angle elementary functions because of its accuracy and parallel processing. The proposed CORDIC architecture requires more area over the reference design, but offer high throughput. The area-delay-accuracy trade-off for different advanced algorithms may be investigated in detail and compared with in future work.

References

[1] J. E. Volder, "The CORDIC trigonometric computing technique," *IRE Trans. Electron. Comput.*, vol. EC-8, pp. 330-334, Sep. 1959.

[2] J. S. Walther, "A unified algorithm for elementary functions," in *Proc. 38th Spring Joint Comput. Conf.*, 1971, pp. 379-385.

[3] Y. H. Hu, "CORDIC-based VLSI architectures for digital signal processing, " *IEEE Signal Process. Mag.*, vol. 9, no. 3, pp. 16-35, Jul. 1992.

[4] P. K. Meher, J. Valls, T.-B. Juang, K. Sridharan, and K. Maharatna, "50 years of CORDIC: Algorithms, architectures and applications," *IEEE Trans. Circuits Syst. I, Reg. Papers*, vol. 56, no. 9, pp. 1893-1907, Sep. 2009.

[5] Y. H. Hu and S. Naganathan, "An angle recoding method for CORDIC algorithm implementation," *IEEE Trans. Comput.*, vol. 42, no. 1, pp. 99-102, Jan. 1993.

[6] Y. H. Hu and H. H. M. Chern, "A novel implementation of CORDIC algorithm using backward angle recoding (BAR)," *IEEE Trans. Comput.*, vol. 45, no. 12, pp. 1370-1378, Dec. 1996.

[7] C.-S. Wu, A.-Y. Wu, and C.-H. Lin, "A high-performance/low-latency vector rotational CORDIC architecture based on extended elementary angle set and trellis-based searching schemes," *IEEE Trans. Circuits Syst. II, Analog Digit. Signal Process.*, vol. 50, no. 9, pp. 589-601, Sep. 2003.

[8] T.-B. Juang, S.-F. Hsiao, and M.-Y. Tsai, "Para-CORDIC: Parallel cordic rotation algorithm," *IEEE Trans. Circuits Syst. I, Reg. Papers*, vol. 51, no. 8, pp. 1515-1524, Aug. 2004.