

An Optimal Selective Harmonic Mitigation Technique in Power System by using Shunt Active Power Filter

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ABSTRACT

This paper shows the method of improving the power quality using shunt active power filter. The proposed topic comprises of PI controller, filter hysteresis current control loop, dc link capacitor. The switching signal generation for filter is from hysteresis current controller techniques. With the all these element shunt active power filter reduce the total harmonic distortion. This paper represents the simulation and analysis of the using three phase three wire system active filter to compensate harmonics. The proposed shunt active filter model uses balanced non-linear load. This paper successfully lowers the THD within IEEE norms and satisfactorily works to compensate current harmonics. The model is made in MATLAB / SIMULINK and successfully reduces the harmonic in the source current

KEYWORDS - Harmonics, shunt active filters, power quality.

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I. INTRODUCTION

Shunt technology has brought drastic increase in the use of power electronic equipments resulting in the increase of harmonics in source current or ac mains current. intensive use of power converters, various non linear loads and increasing use of office equipments like computers ,faxes ,printers are reasons for the increasing harmonics resulting in deterioration if sources current and source voltage. Harmonics causes very serious damage in powers system. Problems like resonance;

Overheating of neutral wire, low power factor, damaging microprocessor based equipment. Traditionally, L-C passive filters were used to solve the problem of harmonics to filter out current harmonics to get sinusoidal supply current .Passive filters are classified as single tune filter

and high pass filter. Passive filters have following disadvantages a) I resonance with the source impedance. b) fixed compensation, large configuration size.

To overcome the problems of passive filters, active filters were developed and used to solve the problem of harmonics the technology of the active filter has improved a lot thereby giving very good results to reduce the problem of harmonics. The power semiconductor devices improved the active filters a lot. Active filters solve the problem of harmonic in industrial area as well as utility Power distribution. The active power filter working performance is based on the techniques used for the generation of reference current. With the development various technologies results the lowering of harmonics below 5% as specifies by

IEEE. Efficient ways of generating reference current are p-q theory, synchronous reference.

Current theory (SRF method). In this paper SRF method has been used. We have many current control technologies for active power filter, but the Hysteresis current controller is proved to be very efficient in terms of fast current controllability and it also very easy to apply when compared to other method like sinusoidal PWM. We can detect harmonics in two ways or two main forms first in time domain and second in the frequency domain the paper deals with Fast Fourier Transform (FFT) is used to find harmonics in frequency domain. Other frequency domain techniques are discrete Fourier transform (DFT); recursive discrete Fourier transform (RDFT).our main target is to reduce THD of supply current with the help of hysteresis band current controller. There are two type of hysteresis Current controller namely, adaptive hysteresis current controller and fixed band current controller. Our paper deals with the use of fixed band hysteresis current controller. The model of shunt active power filter using hysteresis current controller has been used in mat lab/simulink. Results have been successfully retrieved from model and followed by conclusion.

II. SHUNT ACTIVE POWER FILTER

The large scale use of power electronics equipment has led to increase in harmonics in the power system. The nonlinear loads generate harmonic current which distorts the voltage waveform at PCC. These current harmonics will result in a power factor reduction, decrease in efficiency, power system voltage fluctuations and communications interference [3]. So harmonics can be considered as a pollutant which pollutes the entire power system. Traditionally a bank of tuned LC filters was used as a solution for the problems caused by the system harmonics, since they are easy to design, have simple structure, low cost and high efficiency. [7] Phase advancers synchronous capacitors etc. were also employed for the power system quality enhancement. However traditional controllers have many drawbacks. It provides only fixed compensation, generates resonance problems and are bulky in size [6]. To overcome these disadvantages, active power filters are introduced which compensate for the current harmonics and reduces the total harmonic distortion. The SAPF is connected in parallel with the line through a coupling inductor. Its main power circuit consists of a three phase

three-leg current controlled voltage source inverter with a DC link capacitor. An active power filter operates by generating a compensating current with 180 degree phase opposition and injects it back to the line so as to cancel out the current harmonics introduced by the nonlinear load. This will thus suppress the harmonic content present in the line and make the current waveform sinusoidal. So the process comprises of detecting the harmonic component present in the line current, generating the reference current, producing the switching pulses for the power circuit, generating a compensating current and injecting it back to the line. In this paper a nonlinear load supplied by a three phase voltage source is projected. An active power filter is introduced in parallel to this system for the compensation of current harmonics caused by the nonlinear loads. Here SRF algorithm is used for the reference current extraction from the distorted source current, which is being explained in section II. The switching pulses for the power circuit are generated using the Hysteresis Current Control technique as explained in section III and is found to be very effective. The simulation results are projected in section IV. Figure 1 shows a three phase shunt active power filter.

2.1 Working Principle of Shunt APF

The schematic diagram of the single phase load compensator is shown in Figure 25. In this diagram a voltage source is supplying a load that could be nonlinear as well. The point of connection of the load and the source is the point of common coupling (PCC). Since there is no feeder joining the source and the load, we shall designate the source to be stiff. Here the compensator consists of an H-bridge inverter and an interface Inductor (L_f). The resistance R_f represents the resistance of the interface inductor due to its finite Q-factor as well as the losses in the inverter. One end of the compensator is connected at the PCC through the interface inductor while the other end is connected with the load ground. The dc side of the compensator is supplied by a dc capacitor C_{dc} . The inverter is expected to be controlled to maintain a voltage V_{dc} across the capacitor.

Let us assume that the load is nonlinear and draws a load current has a poor power factor. The instantaneous load current then can be decomposed as

$$i_L = i_{Lp} + i_{Lq} + i_{Lh} \dots(1)$$

Where i_{Lp} and i_{Lq} are respectively the real and reactive parts of the current required by the load

and i_{Lh} is the harmonic current drawn by the load. The purpose of the compensator is to inject current i_c such that it cancels out the reactive and harmonic parts of the load current. Now applying KCL at the PCC we get

$$i_L = i_s + i_c \Rightarrow i_s = i_L - i_c \quad (2)$$

We assume that the compensator operates in a hysteresis current control loop in which the compensator current tracks a reference current i_c^* . Let us now choose this reference current as

$$i_c^* = i_{Lq} + i_{Lh} \dots (3)$$

If the inverter accurately tracks this reference current, then the source current will be equal to the unity power factor current drawn by the load. Since the compensator does not draw any real current, the average power consumed by the compensator

2.2 Implementation

The hysteresis band current control (HBCC) technique is used for pulse generation in current controlled VSIs. The control method offers good stability, gives a very fast response, provides good accuracy and has got a simple operation [5]. The HBCC technique employed in an active power filter for the control of line current is shown in Figure 3. It consists of a hysteresis band surrounding the generated error current. The current error is obtained by subtracting the actual filter current from the reference current. The reference current used here is obtained by the SRF method as discussed earlier which is represented as i_{abc}^* . The actual filter current is represented as i_{fabc} . The error signal is then fed to the relay with the desired hysteresis band to obtain the switching pulses for the operation of APF depends on the sequence of pulse generated by the controller. Figure 4 shows the simulation diagram of the hysteresis current controller. A band is set above and below the generated error signal. Whenever this signal crosses the upper band, the output voltage changes so as to decrease the input current and whenever the signal crosses the lower band, the output voltage changes to increase the input current. Accordingly switching signals are generated. [5]

III. SIMULATION RESULTS AND DISCUSSION

The harmonic current compensation is implemented in a three-phase power system using a shunt active power filter. The rms value of source voltage of the system is set as 480V and a combination of three-phase universal bridge rectifier with an RLC load across it constitutes the nonlinear load which introduces the harmonics

into the system. Table 1 shows the various circuit parameters and design specifications used in this simulation. The source current waveform without filter in a-phase is shown in Figure 6. The Total Harmonic Distortion (THD) spectrum in the system without filter is shown in Figure 7, which indicate a THD of 25.75%. The compensating current waveform in a phase is illustrated in Figure 8. The source current after the injection of compensating current is shown in Figure 9. The THD with active power filter included is observed to be 4.87% which is within the allowable harmonic limit. Figure shows the THD spectrum with active power filter in the circuit .

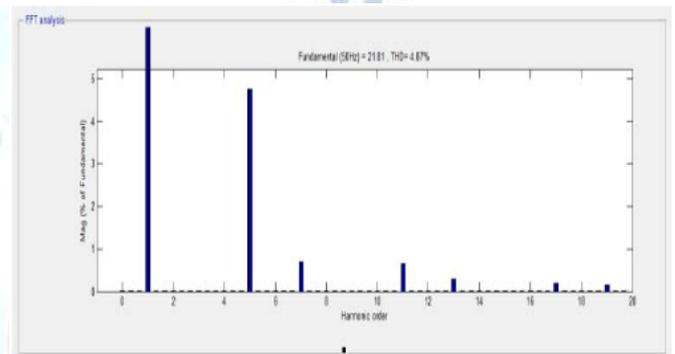


Fig : THD Spectrum with active power filter.

IV. CONCLUSION

The SAPF explained in this paper compensate the line current harmonics generated due to the nonlinear loads in the system. HBCC technique used for the switching pulse generation was found to be effective and its validity is proved based on simulation results. Thus SAPF has been proved to be effective to keep the harmonic content in power lines within the permissible limit of IEEE standards

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