



Power Factor Correction Using Step-Up Chopper Fed to a DC Motor Drive

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ABSTRACT

With the expanding interest for power from the ac line and more stringent cutoff points for power quality, power factor correction is extraordinary consideration as of late. An assortment of circuit topologies and control strategies has been created for the PFC application. While the discontinuous conduction mode (DCM) converters for example, boost and fly back converters are appropriate for low power applications. Continuous conduction mode (CCM) support converters with normal current mode, hysteresis control are absolute for some medium and high power applications. To eliminate these issues a few converter topologies utilizing proper semiconductor devices and control plans have been proposed. This examination is to find a minimal effort, small size, effective ac to dc converter to meet the UPS. In the proposed circuit, the power factor is enhanced by utilizing boost dc to dc converter. It eliminates the utilization of active switch and control circuit for PFC, which brings about higher efficiency. A Matlab/Simulink based model is introduced to get the results. At last a DC motor load is connected and simulation results are presented.

KEYWORDS: AC-DC Converter, Power Factor Correction, Step-up Chopper, etc.

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I. INTRODUCTION

Switched mode Power Factor Corrected (PFC) AC-DC converters with high response and power consistency are being utilized as front end rectifiers for many uses. The converters are either buck or boost topologies. The buck type topology gives variable DC voltage, which is much lower than the input voltage magnitude. But when the instantaneous input voltage is beneath the DC voltage, the current drops to zero that outcome in huge increment in input current THD. Indeed, even with input filter the buck converters give just restricted change in input current quality.

On other hand the boost type converter dependably creates the voltage higher than the input voltage magnitude. The boost inductor with desired choice

keeps up consistent input current with great wave shape. This lead the converter control to keep up close to unity power factor, low input current THD and great voltage regulation. The two stage scheme brings about high power factor and quick response output voltage by utilizing two controllers and improved power stages. The principle disadvantages of this plan are its moderately higher cost and bigger size came about because of its complicated power stage topology and control circuits, especially in low power applications. Keeping in mind the end goal to lessen the cost, the single stage approach, which coordinates the PFC stage with a dc/dc converter into one phase, is produced.

The above Figure.3 demonstrates the closed loop control for PFC ac dc converter comprises of various component. The general outline of the closed loop control of PFC converter is appeared in Fig.3 The goal is to manage the power flow and meet the UPD input performance , for example, output voltage direction $\leq 2\%$, input control consider ≥ 0.95 , input current distortion THD $\leq 5\%$. The output voltage is controlled by the external voltage control loop. The input power factor and current wave shape are controlled by the internal current loop. Both controller are picked as PI sort compensator and spoken to by the transfer function $G_c(s)=K_p(1+1/T_i s)$. Where K_p and T_i are relative pick up and necessary time steady separately. The output voltage is controlled utilizing voltage error (Verror) acquired by looking at the measured genuine output voltage (Vactual) and sought reference voltage (Vref). The Verror is prepared by the voltage PI-controller whose output is the converted current magnitude and limited to most extreme esteem.It is multiplied with unity magnitude extent sinewavereference gotten from input voltage. The output of the multiplier is the sought sinusoidal input reference current signal (i_{ref}) with greatness magnitude and phase angle. This signal is further prepared by the direct current controller as shown in Fig.4 and creates pulse width adjusted gate pulses such an extent that converter maintains the input execution index.

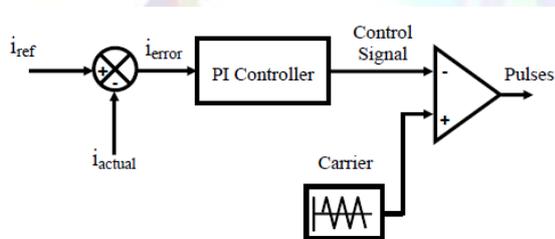


Figure 4 Linear Current Control

The external/voltage loop controller parameter values for K_p furthermore, T_i are intended to keep up steady output voltage independent of disturbances because of progress in load/input voltage. K_p and T_i are found from open loop converter output voltage reaction for a step load change [5]. Though the inward/current loop controller values for K_p what's more, T_i are intended to enhance PWM pulses with the end goal that converter operation keeps up inputcurrent close sinusoidal with constrained distortion and power factor close unity.

IV. MATLAB/SIMULINK MODEL AND SIMULATION RESULTS

In this paper simulation is completed for two cases.

In Case-1: AC to DC transformation without Active Power Factor Correction.

Without Active Power Factor Correction:

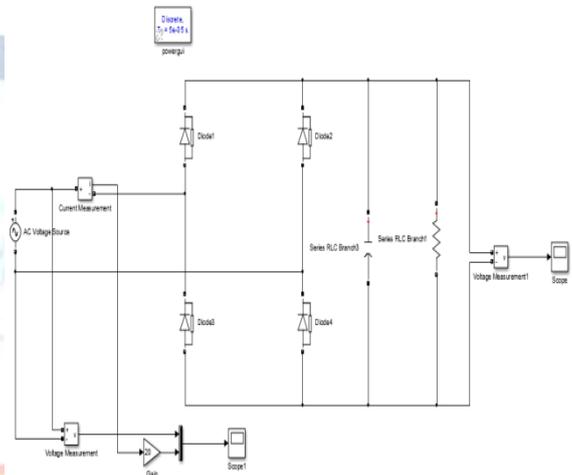


Figure 5 Matlab model without Active Power Factor Correction

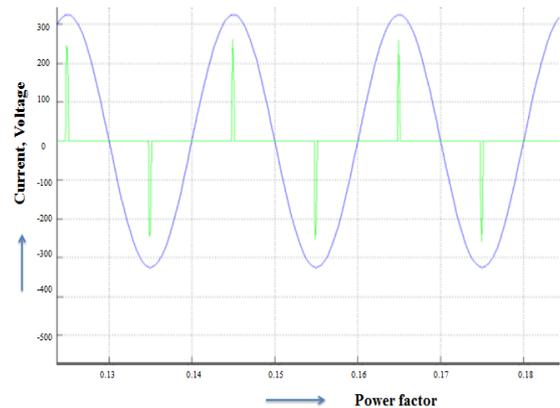


Figure 6 Input Voltage and Current Waveforms

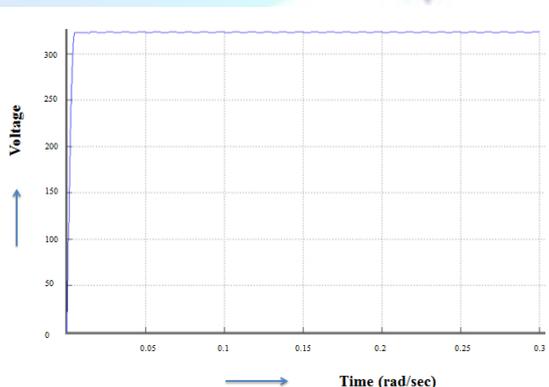


Figure 7 Constant Output Voltage (v) at Load side Waveform

In Case-2: AC to DC transformation with Active Power Factor Correction.

With Active Power Factor Correction:

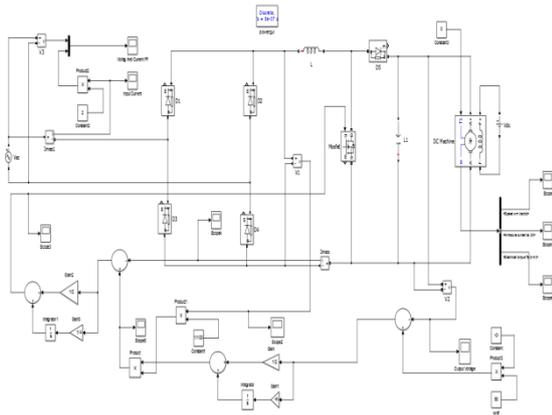


Figure 8 Matlab model with Active Power Factor Correction

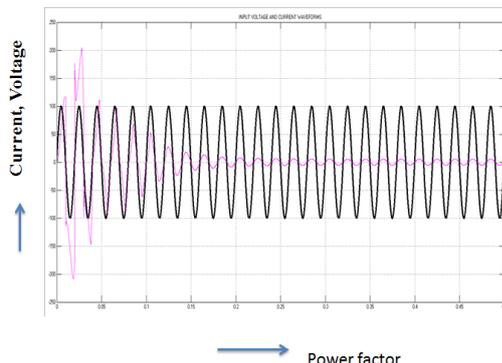


Figure 9 Input Voltage and Current Waveforms

The above waveform shows the AC side Input Voltage and Current in phase with Voltage of Unity Power Factor.

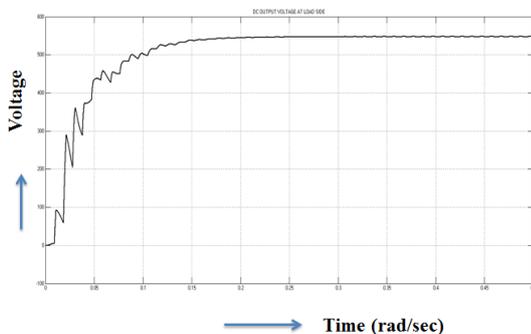


Figure 10 Constant Output Voltage (v) at Load Side Waveform

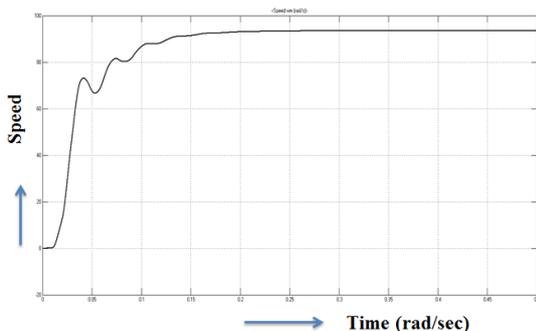


Figure 11 DC Motor Speed (rad/sec) Waveform

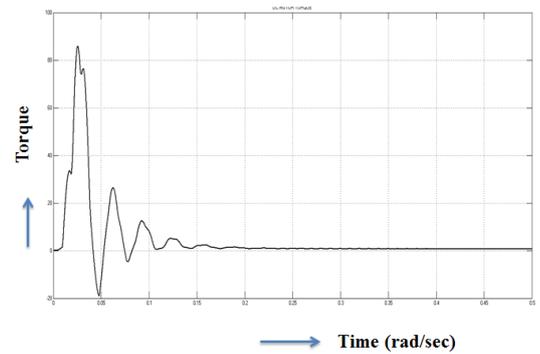


Figure 12 DC Motor Torque (N-m) Waveform

V. CONCLUSION

In this paper, another ac/dc converter in view of a quasi-active PFC scheme has been exhibited. The proposed technique creates a current with low harmonic content to meet the standard particulars and additionally high effectiveness. This circuit depends on adding an auxiliary winding to the transformer of a cascade dc/dc DCM fly back converter. The proposed converter is connected to a DC motor drive. At last a Matlab/Simulink based model is created and simulation results are introduced.

REFERENCES

- [1] Hussain S. Athab, Dylan Dah-Chuan Lu. A High Efficiency AC/DC Converter with Quasi-Active Power Factor Correction Eee Transaction on Power Electronics, Vol. 25, No.5, May 2010, P.P 1103-1109.
- [2] R. Redle, L. Balogh, and N. O. Sokal, new family of single-stage isolated power factor correctors with fast regulation of the output voltage, in Proc. IEEE PESC 1994 Conf., pp. 1137-1144.
- [3] C. Qian and K.Smedley, -A topology survey of single-stage power factor with a boost type input current-shaper, IEEE Trans. Power Electron. vol. 16, no. 3, pp. 360-368, May 2001.
- [4] T.-F. Wu, T.-H. Yu and Y.-C. Liu, -An alternative approach to synthesizing single stage converters with power factor correction feature, IEEE Trans. Ind. Electron., vol. 46, no. 4, pp. 734-748, Aug.1999.
- [5] L. Huber, J. Zhang, M. Jovanovic, and F.C. Lee, -Generalized topologies of single-stage input-current shaping circuits, IEEE Trans. Power Electron., vol. 16, no. 4, pp. 508-513, Jul. 2001.
- [6] H. Wei, I. Batarseh, G. Zhu, and K. Peter, -A single switch ACDC converter with power factor correction, IEEE Tran Power Electron., vol. 15, no. 3, pp. 421-430, May 2000.
- [7] L. K. Chang and H. F. Liu, -A novel forward AC/ converter with input current shaping and fast output voltage regulation via reset winding, IEEE Trans. Ind. Electron., vol. 52, no. 1, pp. 125-131, Feb.2005.

- [8] H. L. Do, –Single-stage single-switch power factor AC/DC converter, *Inst. Electr. Eng. Proc. Electr. Power Appl.*, vol. 152, no. 6, pp. 1578–1584, Nov. 2005.
- [9] J. Qian, Q. Zhao, and F. C. Lee, Single-stage single switch power factor correction ac/dc converters with dc-bus voltage feedback for universal line applications, *IEEE Trans. Power Electron.*, vol. 13, no. 6, pp. 1079–1088, Nov. 1998.
- [10] S. Luo, W. Qiu, W. Wu, and I. Batarseh, –Fly boost power factor correction cell and a new family of single-stage AC/DC converters, *IEEE Trans. Power Electron.*, vol. 20, no. 1, pp. 24–33, Jan. 2005.
- [11] M. M. Jovanovic, D. M. Tsang, and F. C. Lee, Reduction of voltage stress in integrated high quality rectifiers-regulators by variable frequency control, in *Proc. IEEE APEC 1994 Conf.*, pp. 569–575.
- [12] J. Sebastian, A. Fernandez, P. Villegas, M. Hemando, and J. Prieto, –New topologies of active input current shapers to allow AC-to-DC converters with asymmetrically driven transformers to comply with the IEC-10003-2, *IEEE Trans. Power Electron.*, vol. 17, no. 4, pp. 493–501, Jul. 2002.

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